



SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS X1 *2 SLOT
16	PCI SLOT (NA)
17	ITE 8620 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC887-VD2
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX
27	VCORE ISL95812_1

SHEET

TITLE

28	VCORE ISL95812_2
29	RT8120_DDR POWER
30	LPT
31	DVI
32	IT8892E (NA)
33	USB3 VL805

Gigabyte Technology

Cover Sheet

Title	Document Number	Rev
Size Custom	GA-H81M-D2W WG	1.01
Date:	Tuesday, December 31, 2013	Sheet 1 of 33

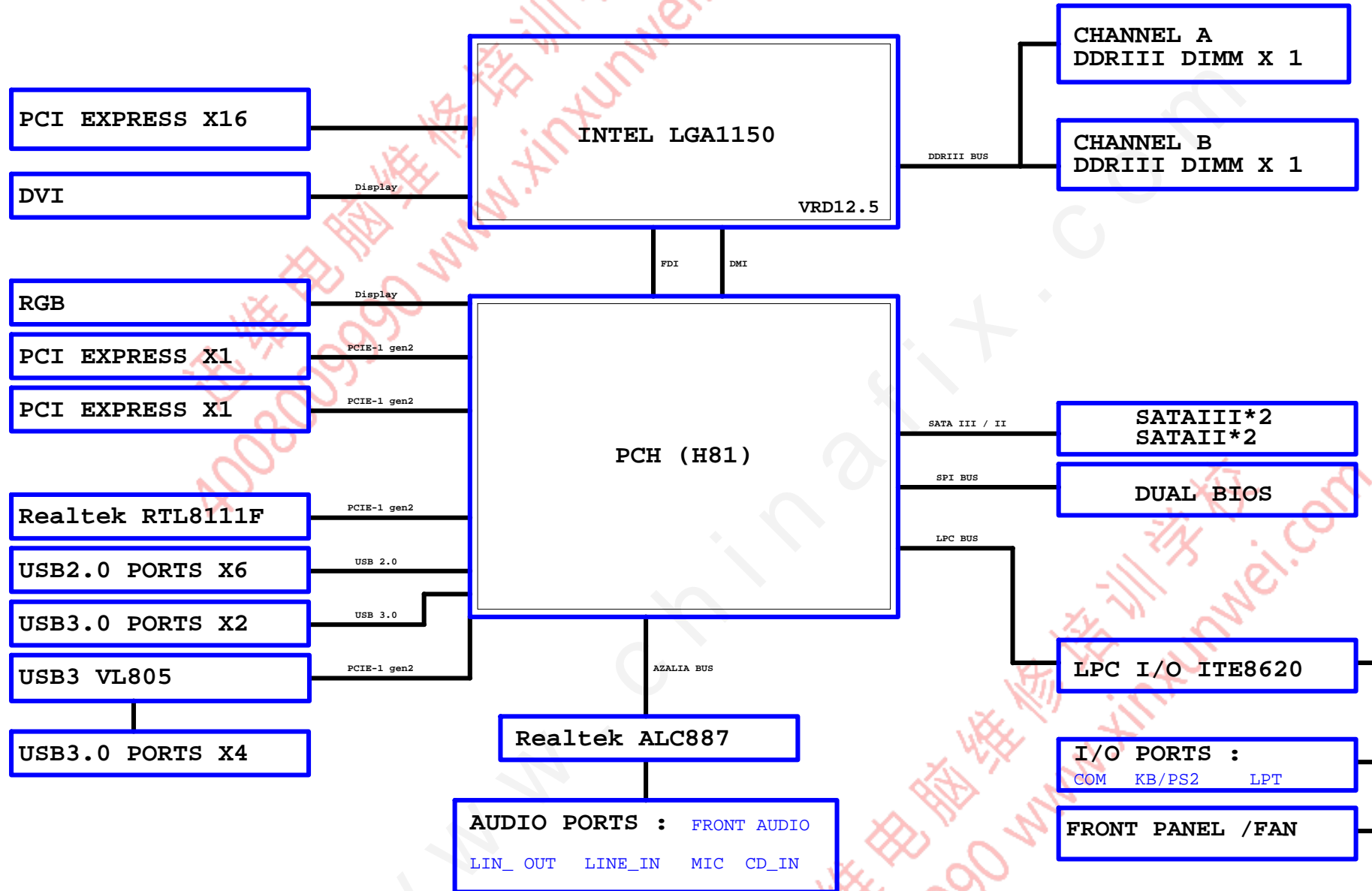
Circuit or PCB layout change

2013/04/08

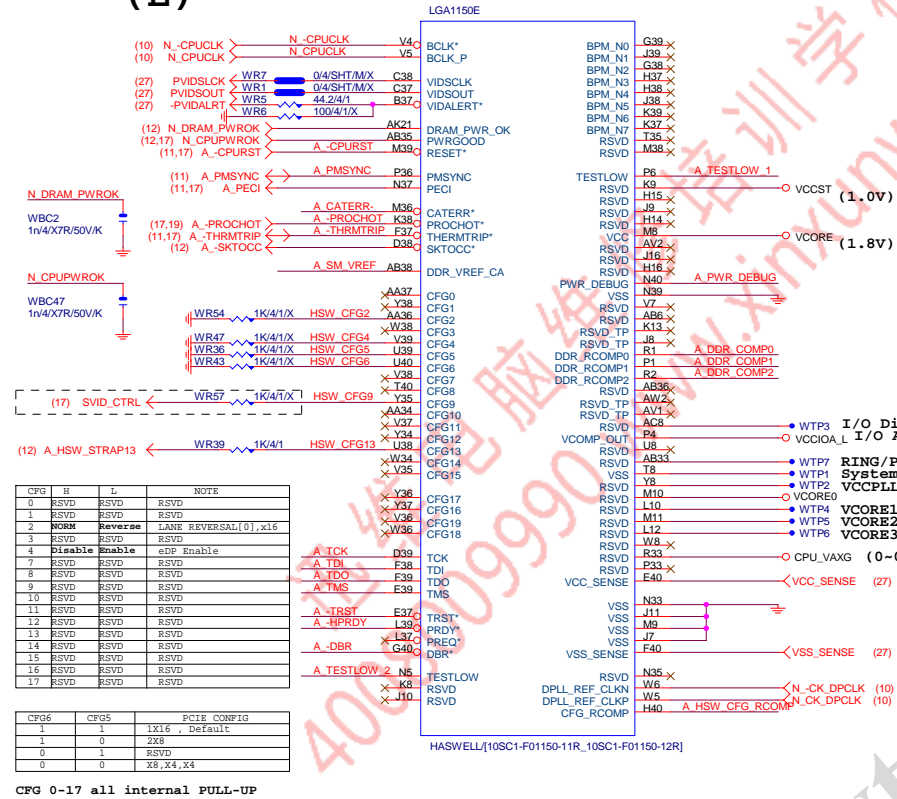
[illegible]

S:單文
4:四層板
V:第二層是VCC
N:咖啡色
B:製程

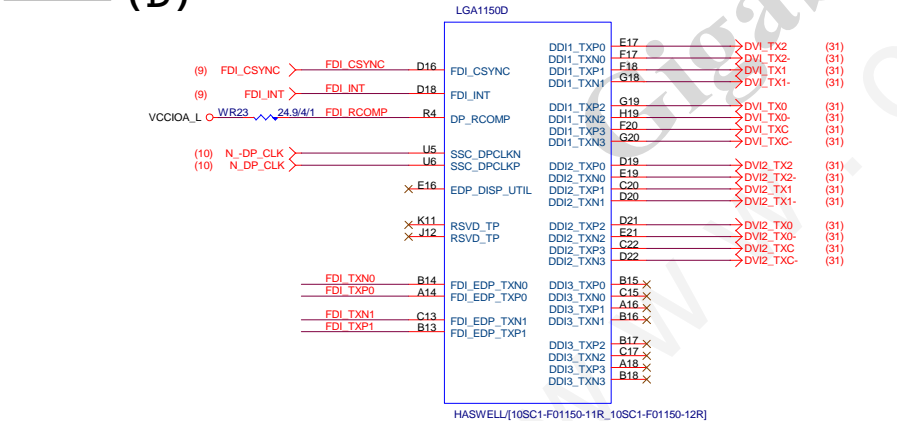
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<p align="center">BOM & PCB MODIFY HISTORY</p>				
Size Custom	Document Number	GA-H81M-D2W WG		Rev 1.01
Date:	Tuesday, December 31, 2013	Sheet	2 of 33	

BLOCK DIAGRAM

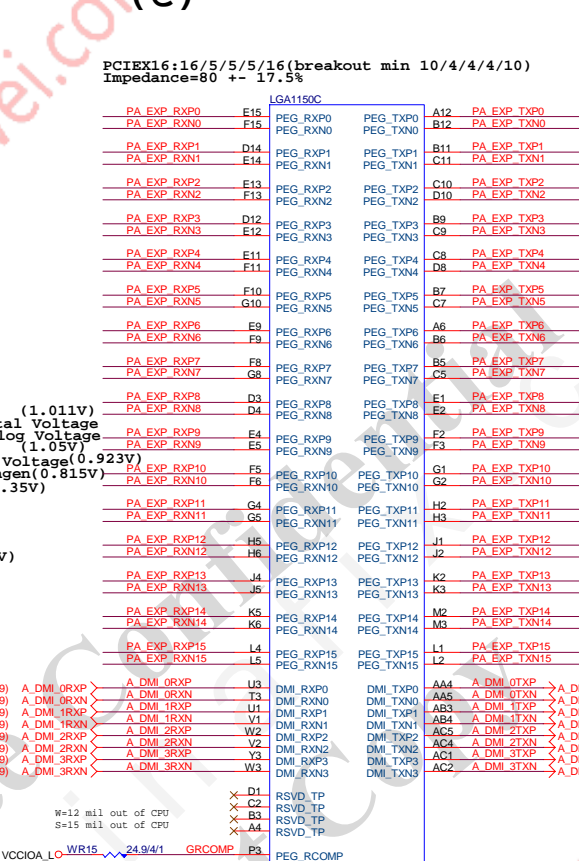
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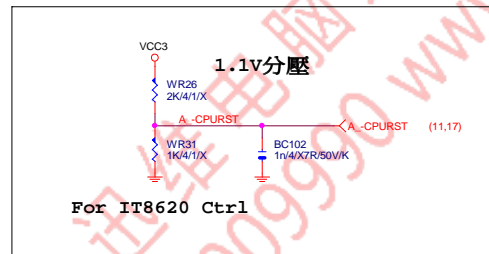
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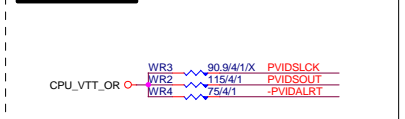
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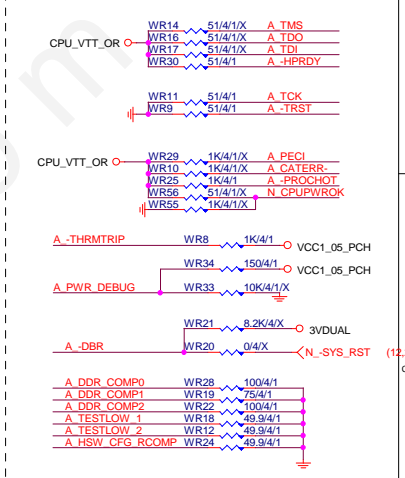
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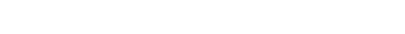
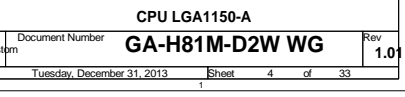
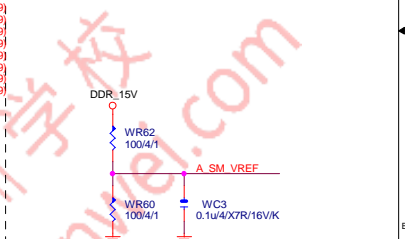
CPU SVID



CPU PU/PD



SM REF



LGA1150

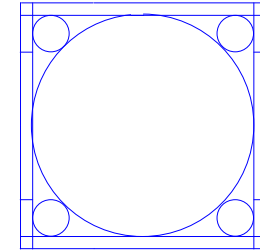
(A)

LGA1150

(B)

LGA1150

(CR)

CR
CPU RETENTION/X

LGA1150



ILM_BP/1156/CSP/ILM_BP/1156/CSP/[12KRC-0F0001-52R_12KRC-0F0001-51R]

DDR BUS

MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0
MAAA1	AV16	DDR0_MA1	DDR0_D01	AD39	MDA1
MAAA2	AU16	DDR0_MA2	DDR0_D02	AF38	MDA2
MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3
MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4
MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5
MAAA6	AW17	DDR0_MA6	DDR0_D06	AE37	MDA6
MAAA7	AT18	DDR0_MA7	DDR0_D07	AF40	MDA7
MAAA8	AU18	DDR0_MA8	DDR0_D08	AH40	MDA9
MAAA9	AT19	DDR0_MA9	DDR0_D09	AH39	MDA10
MAAA10	AW11	DDR0_MA10	DDR0_D10	AK38	MDA10
MAAA11	AV19	DDR0_MA11	DDR0_D11	AK39	MDA11
MAAA12	AU19	DDR0_MA12	DDR0_D12	AH37	MDA12
MAAA13	AY10	DDR0_MA13	DDR0_D13	AH38	MDA13
MAAA14	AT20	DDR0_MA14	DDR0_D14	AK37	MDA14
MAAA15	AU21	DDR0_MA15	DDR0_D15	AK40	MDA15
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17
MODT_A1	AY8	DDR0_ODT1	DDR0_D17	AP38	MDA21
AW8		DDR0_ODT2	DDR0_D18	AP39	MDA19
AW9		DDR0_ODT3	DDR0_D19	AM37	MDA20
AW10			DDR0_D20	AM38	MDA16
AW11			DDR0_D21	AP47	MDA22
AW12			DDR0_D22	AP40	MDA23
AW13			DDR0_D23	AV37	MDA25
AW14			DDR0_D24	AW37	MDA29
AW15			DDR0_D25	AU35	MDA26
AW16			DDR0_D26	AV35	MDA27
AW17			DDR0_D27	AT37	MDA28
AW18			DDR0_D28	AU37	MDA24
AW19			DDR0_D29	AT35	MDA30
AW20			DDR0_D30	AW35	MDA31
AW21			DDR0_D31	AY8	MDA33
AW22			DDR0_D32	AU8	MDA37
AW23			DDR0_D33	AV4	MDA34
AW24			DDR0_D34	AW4	MDA35
AW25			DDR0_D35	AW6	MDA32
AW26			DDR0_D36	AW4	MDA38
AW27			DDR0_D37	AR1	MDA39
AW28			DDR0_D38	AR4	MDA45
AW29			DDR0_D39	AN3	MDA42
AW30			DDR0_D40	AN4	MDA43
AW31			DDR0_D41	AR2	MDA44
AW32			DDR0_D42	AR3	MDA40
AW33			DDR0_D43	AN2	MDA46
AW34			DDR0_D44	AN1	MDA47
AW35			DDR0_D45	AL1	MDA49
AW36			DDR0_D46	AL4	MDA53
AW37			DDR0_D47	AL4	MDA50
AW38			DDR0_D48	AJ4	MDA51
AW39			DDR0_D49	AL2	MDA52
AW40			DDR0_D50	AJ2	MDA48
AW41			DDR0_D51	AJ2	MDA54
AW42			DDR0_D52	AJ1	MDA55
AW43			DDR0_D53	AG1	MDA57
AW44			DDR0_D54	AG4	MDA61
AW45			DDR0_D55	AE3	MDA58
AW46			DDR0_D56	E4	MDA59
AW47			DDR0_D57	AG2	MDA60
AW48			DDR0_D58	AG3	MDA56
AW49			DDR0_D59	AE2	MDA62
AW50			DDR0_D60	AE1	MDA63
AW51			DDR0_D61	AE39	DQSA0
AW52			DDR0_D62	AJ39	DQSA1
AW53			DDR0_D63	AN39	DQSA2
AW54			DDR0_D64	AV36	DQSA3
AW55			DDR0_D65	AV5	DQSA4
AW56			DDR0_D66	AP3	DQSA5
AW57			DDR0_D67	AK3	DQSA6
AW58			DDR0_D68	AF3	DQSA7
AW59			DDR0_D69	AV32	DQSA0
AW60			DDR0_D70	AE38	DQSA1
AW61			DDR0_D71	AJ38	DQSA2
AW62			DDR0_D72	AN38	DQSA3
AW63			DDR0_D73	AJ36	DQSA4
AW64			DDR0_D74	AW5	DQSA5
AW65			DDR0_D75	AP2	DQSA6
AW66			DDR0_D76	AK2	DQSA7
AW67			DDR0_D77	AF2	DQSA7
AW68			DDR0_D78	AJ32	DQSA7

HASWELL[10SC1-F01150-11R_10SC1-F01150-12R]

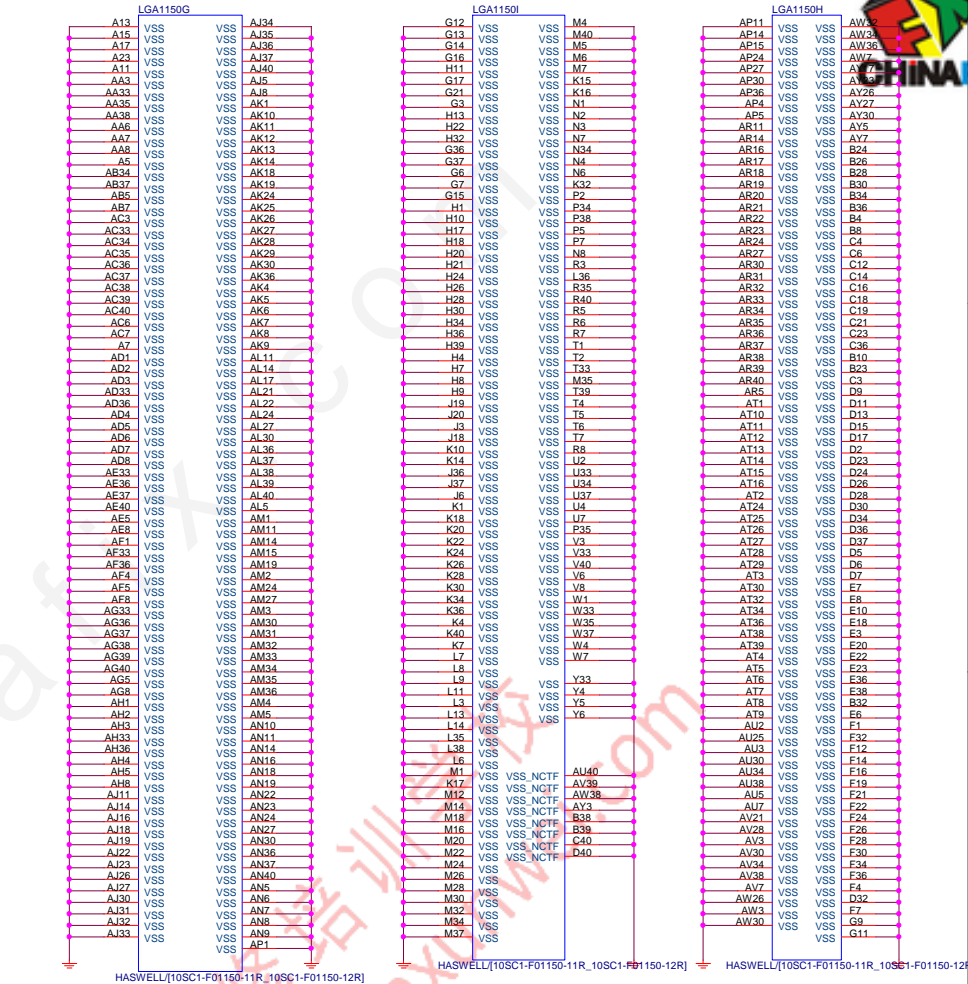
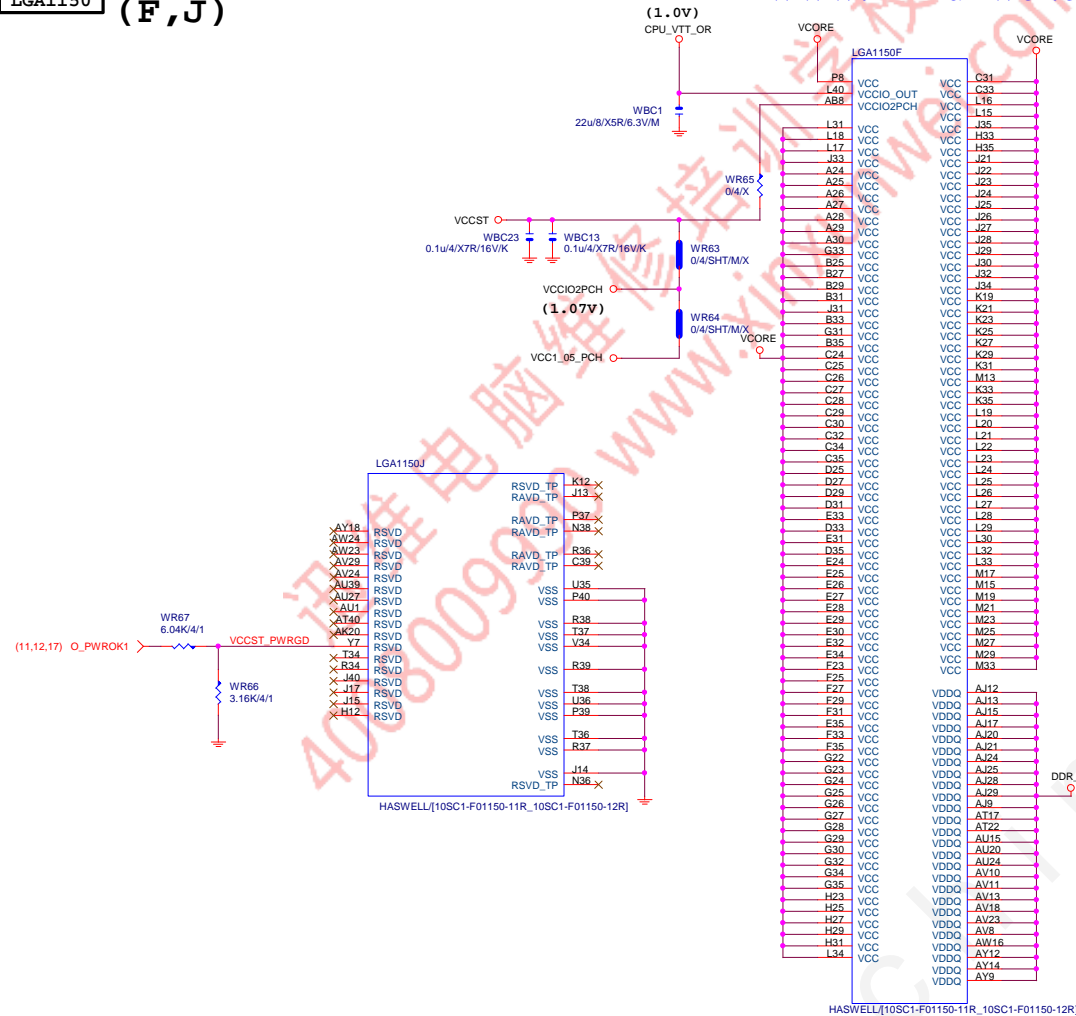
LGA1150B

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MAAB1	AK23	DDR1_MA1	DDR1_D01	AE35	MD81
MAAB2	AM22	DDR1_MA2	DDR1_D02	AG35	MD82
MAAB3	AM23	DDR1_MA3	DDR1_D03	AH35	MD83
MAAB4	AP23	DDR1_MA4	DDR1_D04	AD34	MD84
MAAB5	AL23	DDR1_MA5	DDR1_D05	AG35	MD85
MAAB6	AY24	DDR1_MA6	DDR1_D06	AG34	MD86
MAAB7	AV25	DDR1_MA7	DDR1_D07	AH34	MD87
MAAB8	AU26	DDR1_MA8	DDR1_D08	AL34	MD88
MAAB9	AW25	DDR1_MA9	DDR1_D09	AL35	MD89
MAAB10	AP18	DDR1_MA10	DDR1_D10	AL31	MD810
MAAB11	AK26	DDR1_MA11	DDR1_D11	AK11	MD811
MAAB12	AV26	DDR1_MA12	DDR1_D12	AK34	MD812
MAAB13	AR15	DDR1_MA13	DDR1_D13	AK35	MD813
MAAB14	AV27	DDR1_MA14	DDR1_D14	AK32	MD814
MAAB15	AY28	DDR1_MA15	DDR1_D15	AL32	MD815
			DDR1_D16	AN34	MD817
MODT_B0	AM17	DDR1_ODT0	DDR1_D17	AN31	MD819
MODT_B1	AL16	DDR1_ODT1	DDR1_D18	AP31	MD823
	AM16	DDR1_ODT2	DDR1_D19	AN35	MD820
	AK15	DDR1_ODT3	DDR1_D20	AP35	MD816
			DDR1_D21	AN32	MD818
	AM26	DDR1_ECC0	DDR1_D22	AP32	MD822
	AM25	DDR1_ECC1	DDR1_D23	AM29	MD825
	AP25	DDR1_ECC2	DDR1_D24	AM28	MD828
	AP28	DDR1_ECC3	DDR1_D25	AR29	MD827
	AL26	DDR1_ECC4	DDR1_D26	AR28	MD830
	AL25	DDR1_ECC5	DDR1_D27	AL23	MD824
	AR26	DDR1_ECC6	DDR1_D28	AL28	MD834
	AR25	DDR1_ECC7	DDR1_D29	AP29	MD829
			DDR1_D30	AR12	MD831
SBAB0	AK17	DDR1_BA0	DDR1_D31	AL12	MD833
SBAB1	AL18	DDR1_BA1	DDR1_D32	AL13	MD834
SBAB2	AW28	DDR1_BA2	DDR1_D33	AL12	MD835
			DDR1_D34	AR13	MD836
CKEB0	AW29	DDR1_CKE0	DDR1_D35	AP13	MD837
CKEB1	AY29	DDR1_CKE1	DDR1_D36	AM13	MD838
	AK16	DDR1_CKE2	DDR1_D37	AM12	MD839
	AK15	DDR1_CKE3	DDR1_D38	AR9	MD845
			DDR1_D39	AP9	MD841
CSB0	AP17	DDR1_CS_N0	DDR1_D40	AR6	MD847
CSB1	AN15	DDR1_CS_N1	DDR1_D41	AP6	MD843
	AK17	DDR1_CS_N2	DDR1_D42	AR10	MD844
	AL15	DDR1_CS_N3	DDR1_D43	AP10	MD840
			DDR1_D44	AR7	MD846
			DDR1_D45	AP7	MD842
			DDR1_D46	AM9	MD852
			DDR1_D47	AL9	MD853
			DDR1_D48	AL6	MD850
DCLKB0	AM20	DDR1_CLK_P0	DDR1_D49	AL7	MD855
DCLKB0	AM21	DDR1_CLK_N0	DDR1_D50	AM10	MD848
DCLKB1	AP22	DDR1_CLK_P1	DDR1_D51	AL10	MD849
DCLKB1	AP21	DDR1_CLK_N1	DDR1_D52	AM6	MD854
	AN20	DDR1_CLK_P2	DDR1_D53	AM7	MD851
	AN21	DDR1_CLK_N2	DDR1_D54	AH6	MD861
	AP19	DDR1_CLK_P3	DDR1_D55	AH7	MD860
	AK20	DDR1_CLK_N3	DDR1_D56	AE6	MD859
			DDR1_D57	AE7	MD863
SCASB	AP16C	DDR1_CAS*	DDR1_D58	AJ6	MD856
	AM18C	DDR1_RSVD	DDR1_D59	AJ7	MD857
SRASB	AK16C	DDR1_RAS*	DDR1_D60	AF6	MD858
SWEB		DDR1_WE*	DDR1_D61	AF7	MD862
			DDR1_D62	AF8	MD863
VREF_D0A	AB39	DDR_VREF_D00	DDR1_D63	AF35	DSB80
VREF_D0B	AB40	DDR_VREF_D01	DDR1_D64	AF33	DSB81
			DDR1_D65_P1	AN23	DSB82
			DDR1_D65_P2	AN18	DSB83
			DDR1_D65_P3	AN12	DSB84
			DDR1_D65_P4	AL8	DSB86
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			DDR1_D65_P115	AN18	DSB83
			DDR1_D65_P116	AN12	DSB84
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			DDR1_D65_P120	AF35	DSB81
			DDR1_D65_P121	AN23	DSB82
			DDR1_D65_P122	AN18	DSB83
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			DDR1_D65_P145	AL8	DSB86
			DDR1_D65_P146	AG7	DSB87
			DDR1_D65_P147	AF34	DSB80
			DDR1_D65_P148	AF35	DSB81
			DDR1_D65_P149	AN23	DSB82
			DDR1_D65_P150	AN18	DSB83
			DDR1_D65_P151	AN12	DSB84
			DDR1_D65_P152	AL8	DSB86
			DDR1_D65_P153	AG7	DSB87
			DDR1_D65_P154	AF34	DSB80
			DDR1_D65_P155	AF35	DSB81
			DDR1_D65_P156	AN23	DSB82
			DDR1_D65_P157	AN18	DSB83
			DDR1_D65_P158	AN12	DSB84
			DDR1_D65_P159	AL8	DSB86
			DDR1_D65_P160	AG7	DSB87
			DDR1_D65_P161	AF34	DSB80
			DDR1_D65_P162	AF35	DSB81
			DDR1_D65_P163	AN23	DSB82
			DDR1_D65_P164	AN18	DSB83
			DDR1_D65_P165	AN12	DSB84
			DDR1_D65_P166	AL8	DSB86
			DDR1_D65_P167	AG7	DSB87
			DDR1_D65_P168	AF34	DSB80
			DDR1_D65_P169	AF35	DSB81
			DDR1_D65_P170	AN23	DSB82
			DDR1_D65_P171	AN18	DSB83
			DDR1_D65_P172	AN12	DSB84
			DDR1_D65_P173	AL8	DSB86
			DDR1_D65_P174	AG7	DSB87
			DDR1_D65_P175	AF34	DSB80
			DDR1_D65_P176	AF35	DSB81
			DDR1_D65_P177	AN23	DSB82
			DDR1_D65_P178	AN18	DSB83
			DDR1_D65_P179	AN12	DSB84
			DDR1_D65_P180	AL8	DSB86
			DDR1_D65_P181	AG7	DSB87
			DDR1_D65_P182	AF34	DSB80
			DDR1_D65_P183	AF35	DSB81
			DDR1_D65_P184	AN23	DSB82
			DDR1_D65_P185	AN18	DSB83
			DDR1_D65_P186	AN12	DSB84
			DDR1_D65_P187	AL8	DSB86
			DDR1_D65_P188	AG7	DSB87
			DDR1_D65_P189	AF34	DSB80
			DDR1_D65_P190	AF35	DSB81
			DDR1_D65_P191	AN23	DSB82
			DDR1_D65_P192	AN18	DSB83
			DDR1_D65_P193	AN12	DSB84
			DDR1_D65_P194	AL8	DSB86

LGA1150 (F,J)

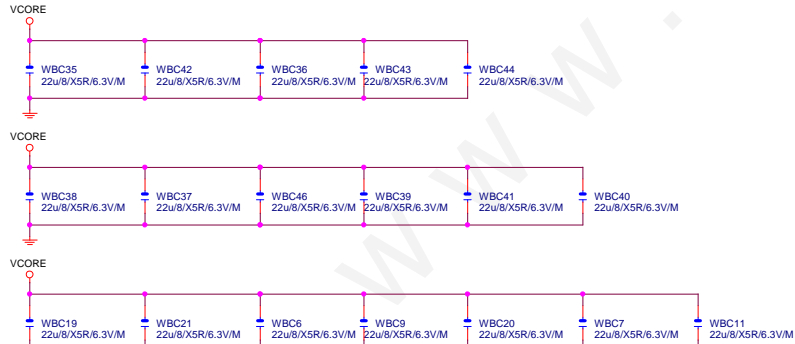
www.xinxunwei.com 400-800-9990

LGA1155 (G,H,I)



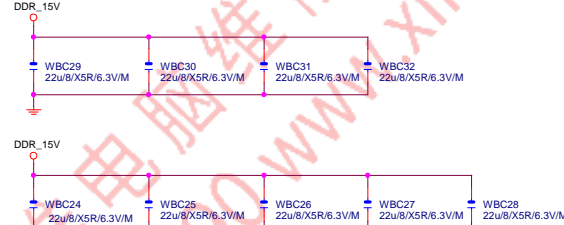
VCore CAP

(X18)



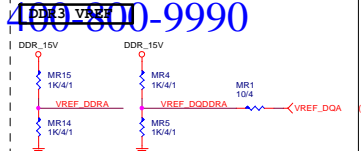
DDR CAP

(X9)




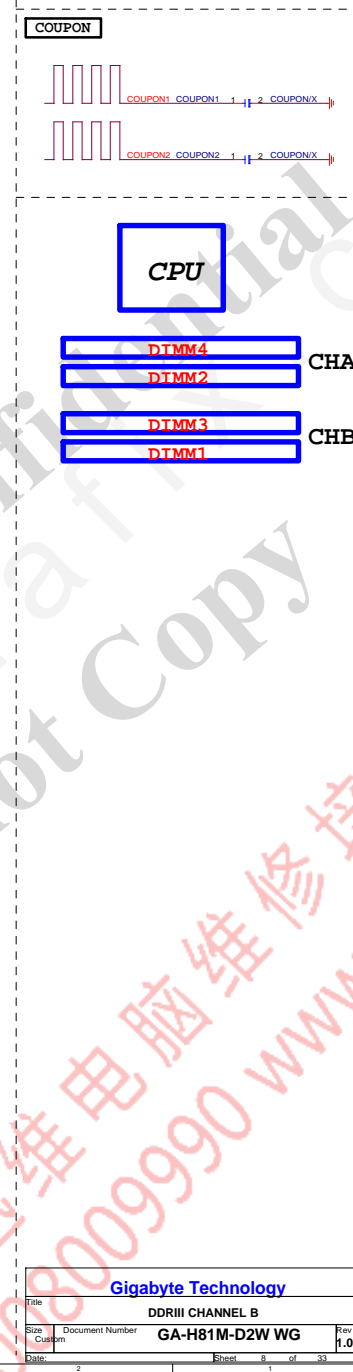
Gigabyte Technology

Title				
CPU LGA1150-C				
Size	Document Number	GA-H81M-D2W WG		Rev
Custom				1.0
Date:	Tuesday, December 31, 2013	Sheet	6	of 33



The diagram shows two circuit connections for DDRVTT. The top connection shows DDRVTT connected to MB40 (10uB/XSR/6.3V/M) through a single diode. The bottom connection shows DDRVTT connected to MB18 (1u4/XSR/6.3V/K) and MB10 (0.1u4/X7R/16V/K) through two diodes in parallel.

 Gigabyte Technology			
Title			
DDRIII CHANNEL A			
Size	Document Number		Rev
Custom	GA-H81M-D2W WG		1.0
Date:		Sheet 7 of 33	



PCH (B)

DMI:12/4/4/4/12(breakout min 8/4/4/4/8)
Impedance=85 +- 17.5%

(4) A_DMI_0TXN A_DMI_0TXN L24
(4) A_DMI_0TXP A_DMI_0TXP K24
(4) A_DMI_0RXN A_DMI_0RXN C20
(4) A_DMI_0RXP A_DMI_0RXP B20
(4) A_DMI_1TXN A_DMI_1TXN G24
(4) A_DMI_1TXP A_DMI_1TXP H24
(4) A_DMI_1RXN A_DMI_1RXN B21
(4) A_DMI_1RXP A_DMI_1RXP F26
(4) A_DMI_2TXN A_DMI_2TXN G26
(4) A_DMI_2TXP A_DMI_2TXP B22
(4) A_DMI_2RXN A_DMI_2RXN C22
(4) A_DMI_2RXP A_DMI_2RXP K26
(4) A_DMI_3TXN A_DMI_3TXN L26
(4) A_DMI_3TXP A_DMI_3TXP A24
(4) A_DMI_3RXN A_DMI_3RXN B24
(4) A_DMI_3RXP A_DMI_3RXP B24

W=4 mil out of PCH
S=15 mil out of PCH

VCC1_5_PCH NR50 7.5K/4/1 DMI_COMP B19
NR40 7.5K/4/1 PCIE_COMP C13
CK_SRCCLK_PCH G22
CK_SRCCLK_PCH F22

PCIE Only

8111G [(24) LA_ML_IN (24) LA_ML_IP (24) LA_ML_ON (24) LA_ML_OP
v1805 [(33) USB3_IN1 (33) USB3_IN1 (33) USB3_ON1 (33) USB3_OP1
PCIEEx1 [(15) PL_PCIE1_IN (15) PL_PCIE1_IP (15) PL_PCIE1_ON (15) PL_PCIE1_OP
(15) PJ_PCIE1_IN (15) PJ_PCIE1_IP (15) PJ_PCIE1_ON (15) PJ_PCIE1_OP

N/A

放靠近 Device & PCI-E Slot
Impedance=80 +- 17.5%

PCIEEX1:16/5/5/5/16 (breakout min 8/4/4/4/8)

PCH (J)

PCHJ
AT1 VSS_NCTF TP22 U11
AT41 VSS_NCTF TP23 U10
AU1 VSS_NCTF TP21 AJ14
AV1 VSS_NCTF TP20 AK14
AV2 VSS_NCTF TP14 K34
AV40 VSS_NCTF TP15 K33
AV41 VSS_NCTF TP12 AH24
AW2 VSS_NCTF TP10 L16
AW40 VSS_NCTF TP11 K16
B40 VSS_NCTF TP9 AM34
B41 VSS_NCTF TP3 R12
C41 VSS_NCTF TP4 N12
D1 VSS_NCTF TP1 L22
D41 VSS_NCTF TP2 K22
TP5 R4
TP6 K5
TP7 P5
TP8 L5
VSS AC31
VSS AF3
VSS AV21

H81/S

USB2.0 : 12/4.5/7.5/4.5/12 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%

PCHB

B85: Port 6/7 N/A
H81: Port 6/7/12/13 N/A

USBN_0 AV10 N-USBP0 (21)
USBP_0 AU10 N+USBP0 (21)
USBN_1 AV11 N-USBP1 (21)
USBP_1 AU11 N+USBP1 (21)
USBN_2 AV12 N-USBP2 (21)
USBP_2 AU12 N+USBP2 (21)
USBN_3 AV13 N-USBP3 (21)
USBP_3 AU13 N+USBP3 (21)
USBN_4 AV14 N-USBP4 (18)
USBP_4 AU14 N+USBP4 (18)
USBN_5 AV15 N-USBP5 (18)
USBP_5 AU15 N+USBP5 (18)
USBN_6 AV16 N-USBP6 (21)
USBP_6 AU16 N+USBP6 (21)
USBN_7 AV17 N-USBP7 (21)
USBP_7 AU17 N+USBP7 (21)
USBN_8 AV18 N-USBP8 (21)
USBP_8 AU18 N+USBP8 (21)
USBN_9 AV19 N-USBP9 (21)
USBP_9 AU19 N+USBP9 (21)
USBN_10 AV20 N-USBP10 (21)
USBP_10 AU20 N+USBP10 (21)
USBN_11 AV21 N-USBP11 (21)
USBP_11 AU21 N+USBP11 (21)
USBN_12 AV22 N-USBP12 (21)
USBP_12 AU22 N+USBP12 (21)
USBN_13 AV23 N-USBP13 (21)
USBP_13 AU23 N+USBP13 (21)

H81: Port 6/7/12/13 N/A

OC0B GP59 AE40 N-USBOC_F (18,21)
OC1B GP40 AE37 N-USBOC_R (18)
OC2B GP41 AD39
OC3B GP42 AD40
OC4B GP43 AE39
OC5B GP9 AC41
OC6B GP10 AC40
OC7B GP14 AG40 N_GPIO14
USBRBIASB NR47 22.6/4/1
USBRBIASB AU20
AP11 CK_DOTCLK
AM11 CK_DOTCLK

NR130 8.2K/4
N_GPIO14 0.3V DUAL
NBC82 0.1u/4/X7R/16V/K
NBC83 0.1u/4/X7R/16V/K

PCH (F)

(21) PCH_USB3_RXN0 F20
(21) PCH_USB3_RXP0 G20
(21) PCH_USB3_TXN0 B18
(21) PCH_USB3_TXP0 C18
(21) PCH_USB3_RXN1 G18
(21) PCH_USB3_RXP1 H18
(21) PCH_USB3_TXN1 B15
(21) PCH_USB3_TXP1 B16

N/A

VCC3 NR62 8.2K/4 AK28
NR63 8.2K/4 AT34

PCHF

USB3 FDI LINK
USB3_RXN_0 FDI_RXN_0 N1 FDI_TXN0
USB3_RXP_0 FDI_RXP_0 N2 FDI_TXP0
USB3_TXN_0 FDI_TXN_0 P2 FDI_TXN1
USB3_TXP_0 FDI_TXP_0 P3 FDI_TXP1

USB3_RXN_1 FDI_CSXNC L2 FDI_CSXNC (4)
USB3_RXP_1 FDI_CSXNC L2 FDI_CSXNC (4)
USB3_TXN_1 FDI_INT L3 FDI_INT (4)
USB3_TXP_1 FDI_INT L3 FDI_INT (4)
USB3_RXN_4 NR29 7.5K/4/1 VCC1_5_PCH
USB3_RXP_4 NR29 7.5K/4/1 VCC1_5_PCH
USB3_TXN_4 NR29 7.5K/4/1 VCC1_5_PCH
USB3_TXP_4 NR29 7.5K/4/1 VCC1_5_PCH

USB3_RXN_5
USB3_RXP_5
USB3_TXN_5
USB3_TXP_5
TACH6_GP70
TACH7_GP71

H81/S
FDI_TXP0_11 FDI_TXP0[0..1] (4)
FDI_TXN0_11 FDI_TXN0[0..1] (4)

USB3.0:20/5/7/5/20 (breakout min 8/4/4/4/8) ; ONLY 3 VIAS
Impedance=85 +- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

PCH CLK PD

CK_SRCCLK_PCH NR89 8.2K/4
CK_SRCCLK_PCH NR88 8.2K/4

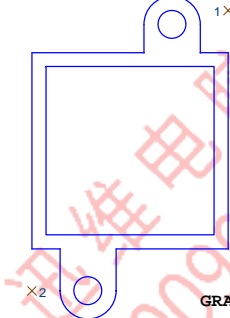
Mount for integrated clock Generation Mode

CK_DOTCLK NR92 8.2K/4
CK_DOTCLK NR91 8.2K/4
NR225 short to GND in non graphic SKU

PCH H/S

LOW COST ICH7 HEATSINK

SB_HEATSINK



GRAY HS

PCH_HS
PCH_HS[12SP2-030005-41R]

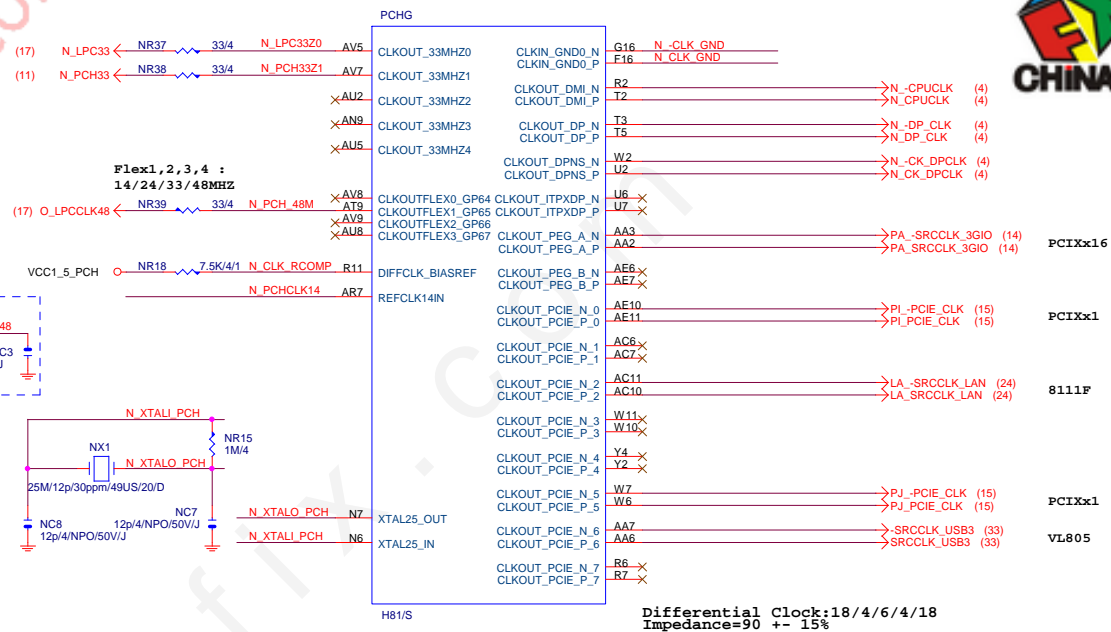
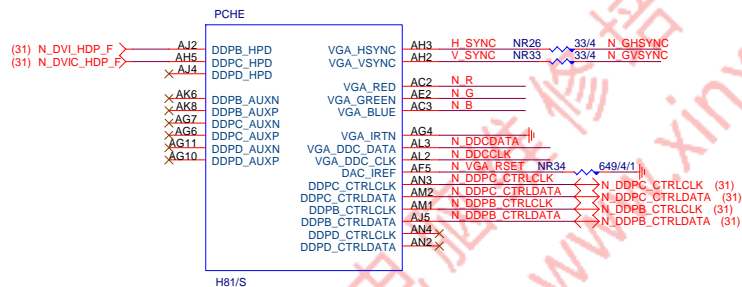
USB TABLE

OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)

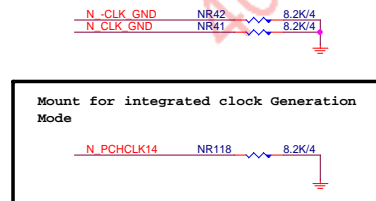
USB OC#	Configure
OC0#	F_USB30
OC1#	USB_LAN
OC2#	R_USB30
OC3#	N/A
OC4#	F_USB1
OC5#	F_USB2
OC6#	KB_MS_USB
OC7#	Not Use

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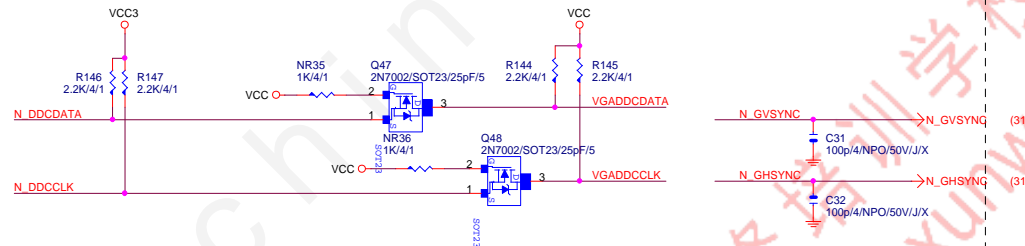
Title	PCH FDI,DMI,USB,PCIE,NVRAM		
Size	Document Number	GA-H81M-D2W WG	
Custom			Rev 1.01
Date:	Tuesday, December 31, 2013	Sheet 9 of 33	1



PCH CLK PD

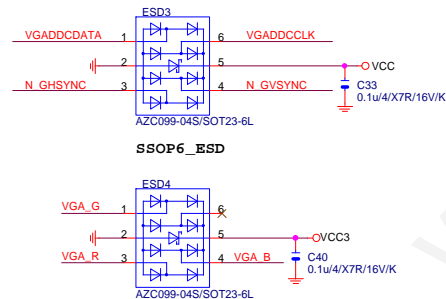


VGA DDC

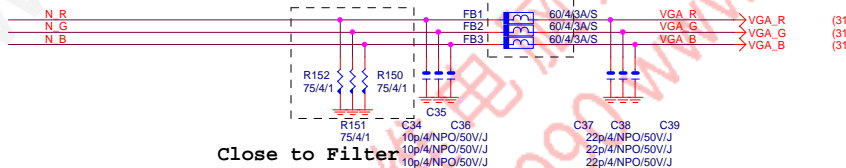


VGA CONNECTOR

VGA ESD

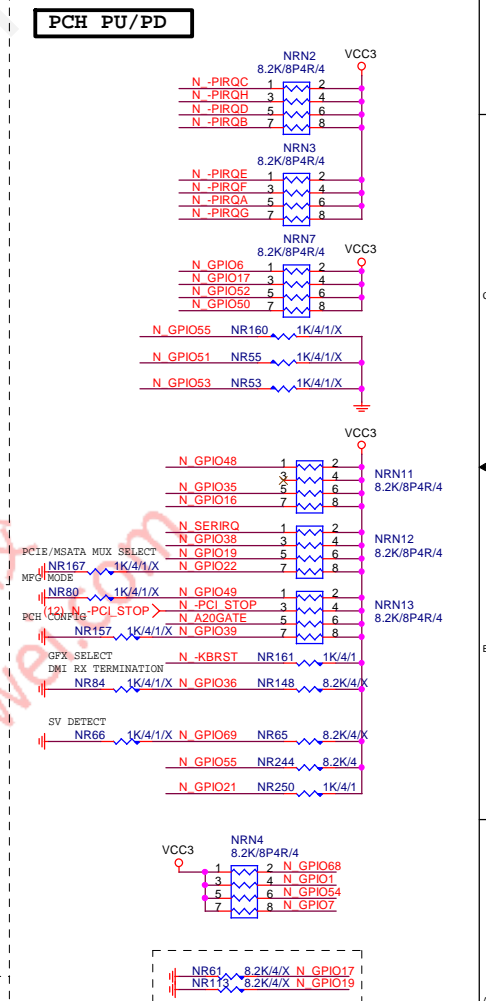
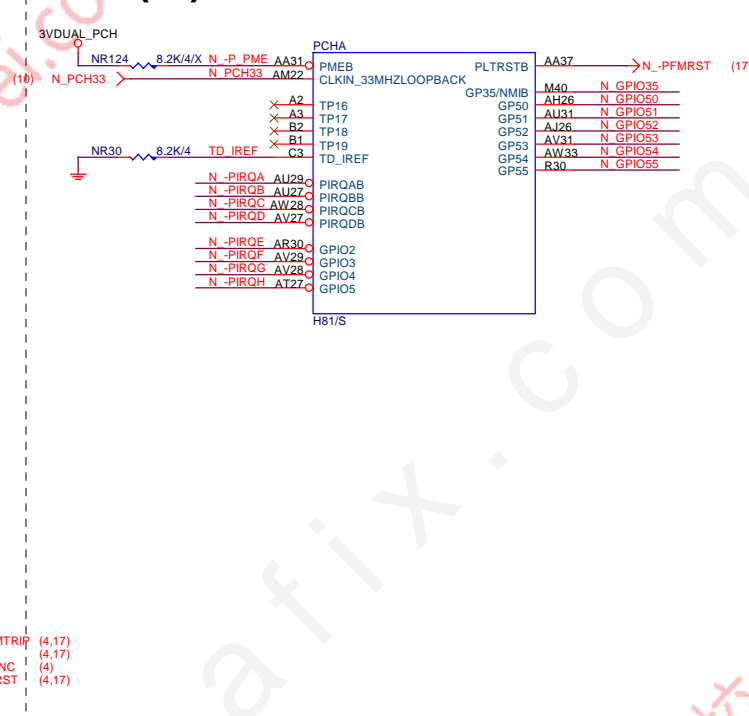


VGA DDC

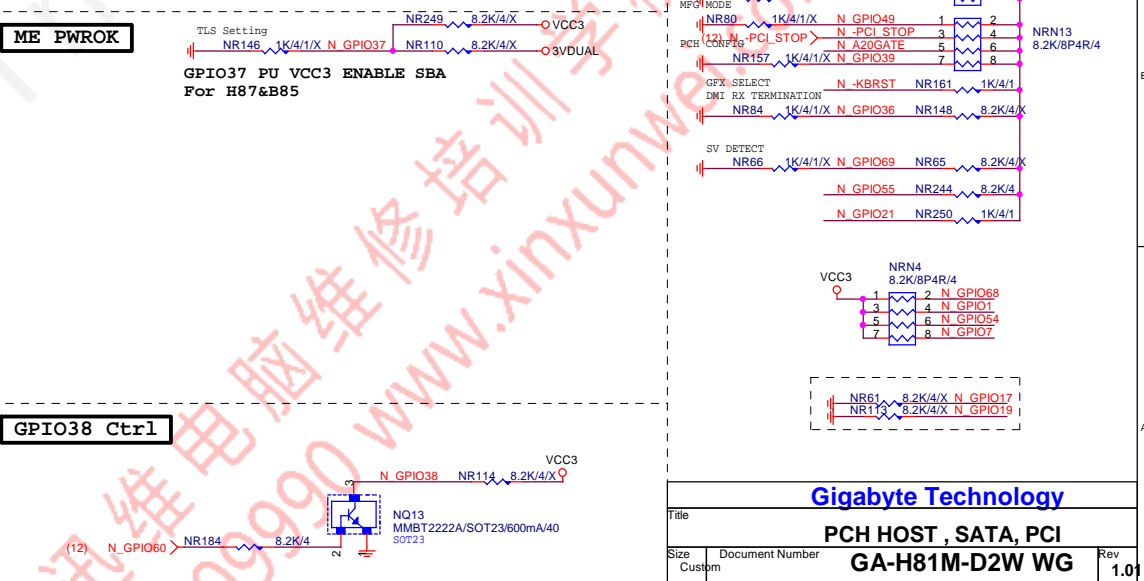


Gigabyte Technology

Title				PCH DISPLAY_CLK BUFFER	
Size				GA-H81M-D2W WG	
Date				Tuesday, December 31, 2013	Sheet 10 of 33



ME PWROK



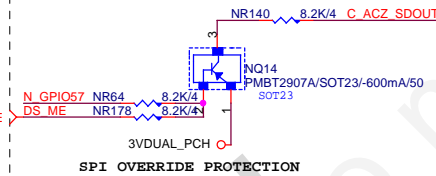
Gigabyte Technology

PCH HOST , SATA, PCI
GA-H81M-D2W WC

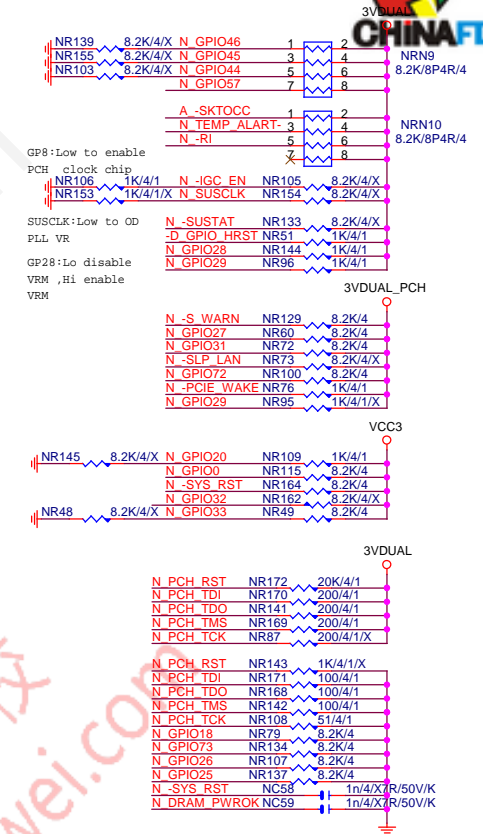
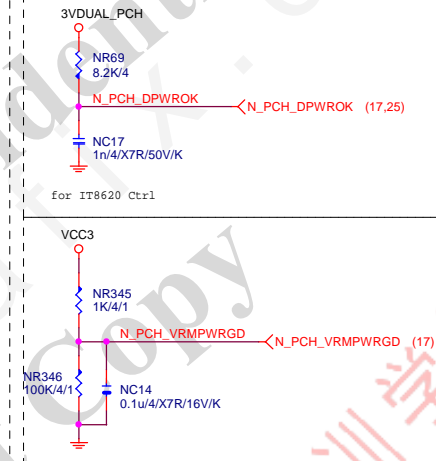
Title			
PCH HOST , SATA, PCI			
Size	Document Number	Rev	
Custom	GA-H81M-D2W WG	1.0	
Date:	Tuesday, December 31, 2013	Sheet	11 of 33

ACZ_SDOUT

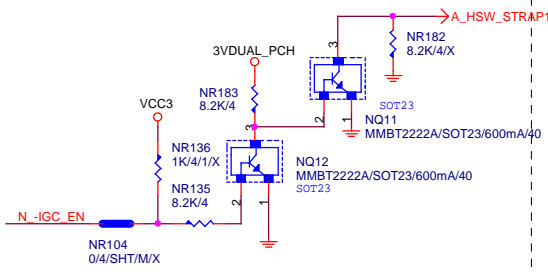
PCH	PU/PD
-----	-------



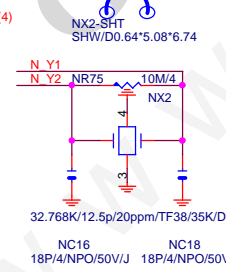
PCH_DPWROK



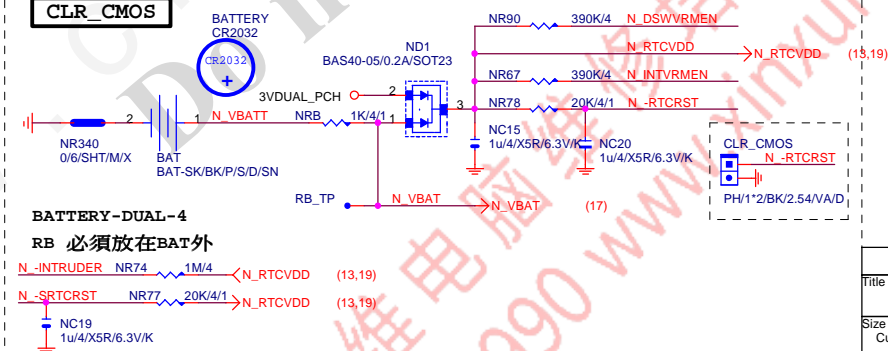
HSW_STRAP13



32.768KHZ



CLR_CMOS



Gigabyte Technology

Title			
PCH GPIO, CTRL, AUDIO			
Size	Document Number	Rev	
Custom	GA-H81M-D2W WG	1.0	
Date:	Tuesday, December 31, 2013	Sheet	12 of 33

SHT PWR



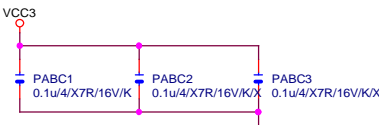
VCC3_ME 3VDUAL_PCH



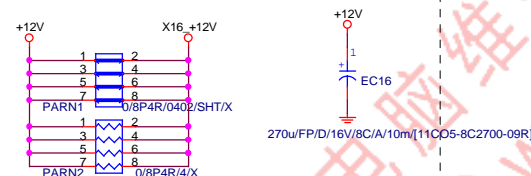
PCH



PCIEX16 CAP



PCIEX16 PROTECT SHT



PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXP0.[15] >>> PA_EXP_RXP[0..15] (4)

PA EXP RXN0.[15] >>> PA_EXP_RXN[0..15] (4)

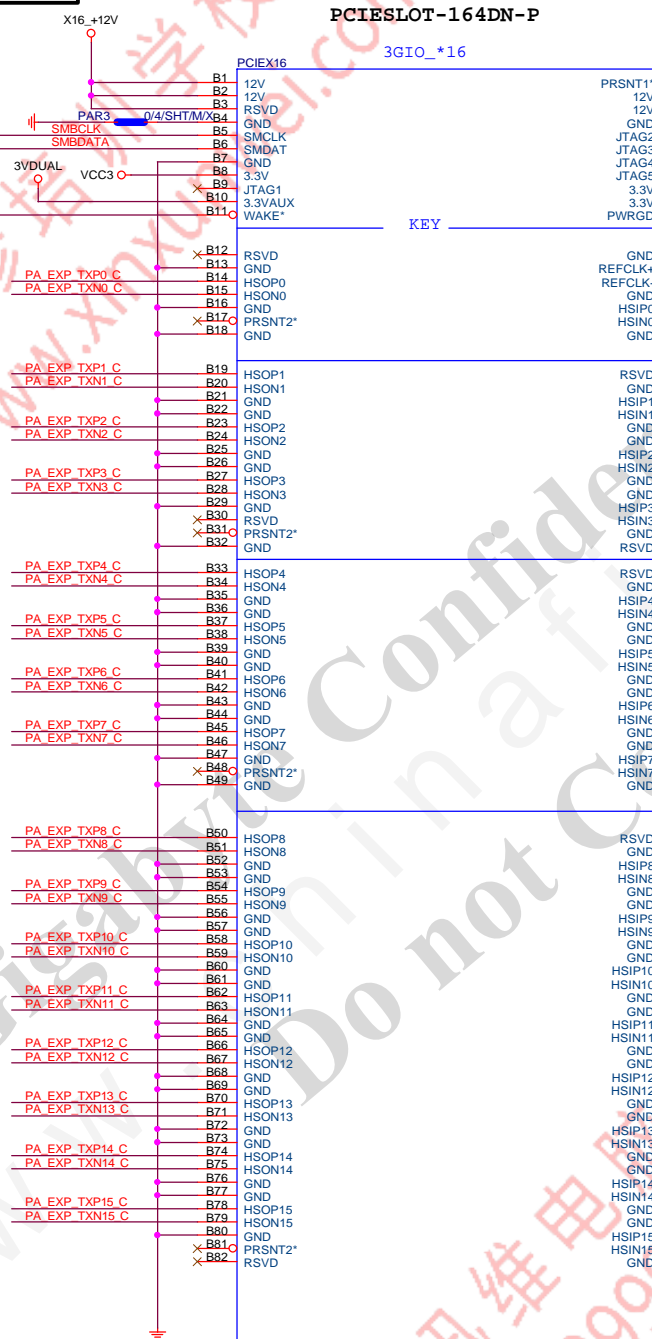
PA EXP TXP0.[15] >>> PA_EXP_TXP[0..15] (4)

PA EXP TXN0.[15] >>> PA_EXP_TXN[0..15] (4)

The auxiliary reset circuit is only required for PCIe Gen3 margining and functional link training

PCIEX16 SLOT

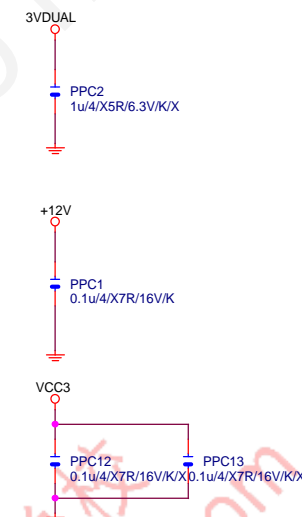
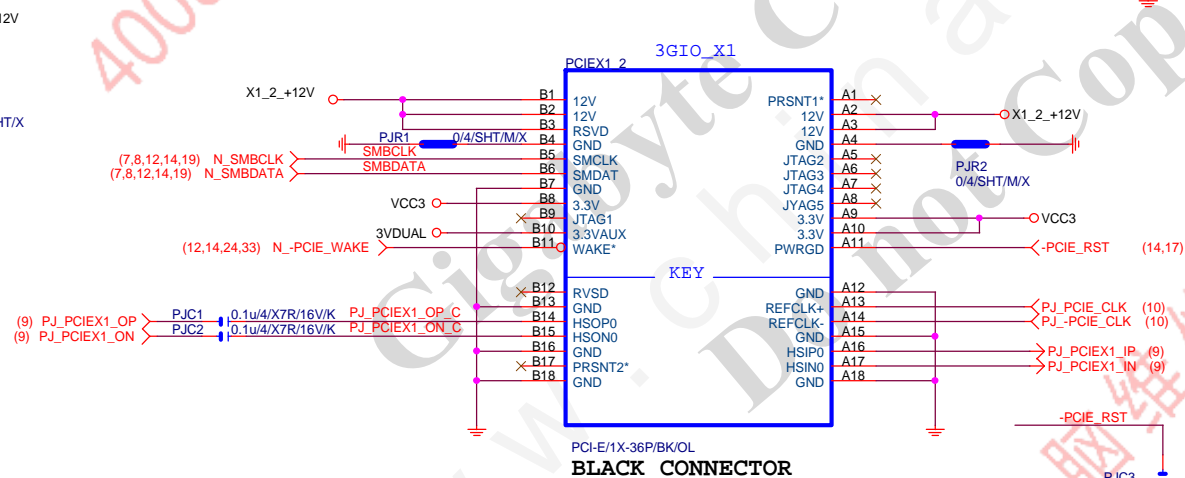
(7,8,12,15,19) N_SMBCLK
(7,8,12,15,19) N_SMBDATA
(12,15,24,33) N_-PCIE_WAKE



PCI-E/16X-164P/BK/LONG DOUBLE

BLACK CONNECTOR

Gigabyte Technology		
Title		
PCI EXPRESS * 16		
Size	Document Number	Rev
Custom	GA-H81M-D2W WG	1.01
Date:	Tuesday, December 31, 2013	Sheet 14 of 33





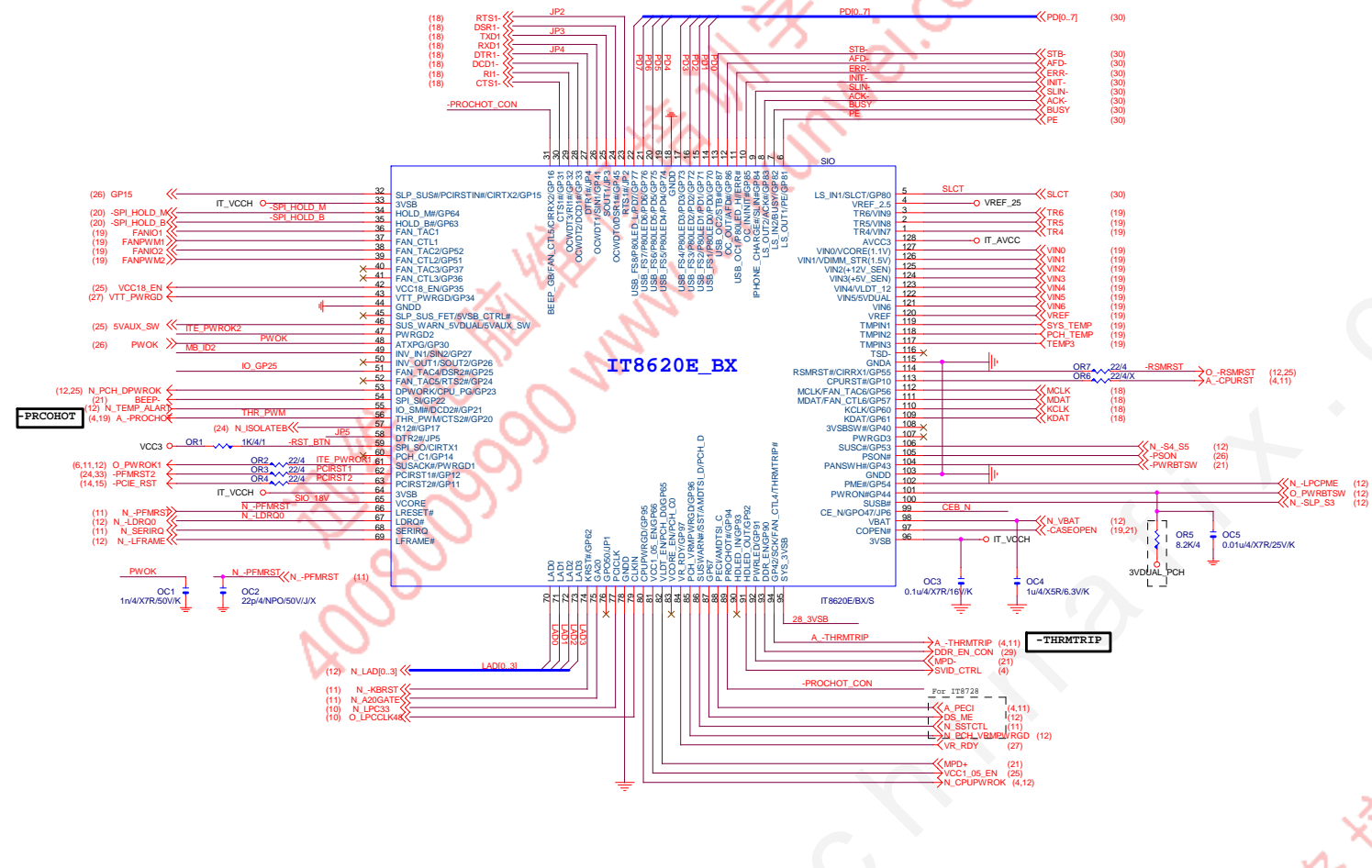
迅维电脑维修培训学校
4008009990 www.xinxunwei.com

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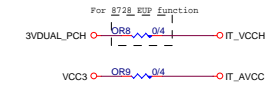
Do not Copy

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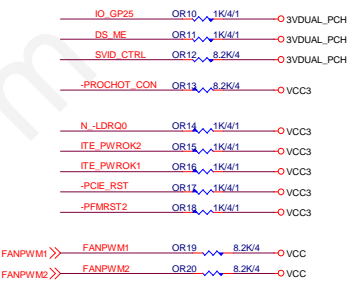
Gigabyte Technology			
Title			
PCI SLOT 1&2			
Size	Document Number	Rev	
Custom	GA-H81M-D2W WG	1.01	
Date:	Tuesday, December 31, 2013	Sheet	16 of 33
	2		1



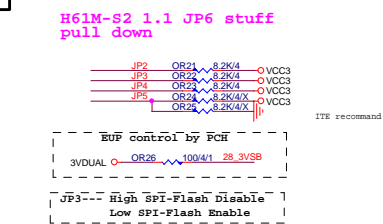
PWR SHT



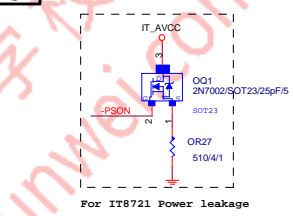
SIO PU



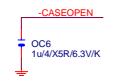
SIO STRAP



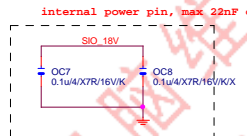
Power leakage



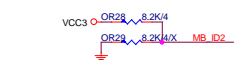
DUAL BIOS OPT STRAP



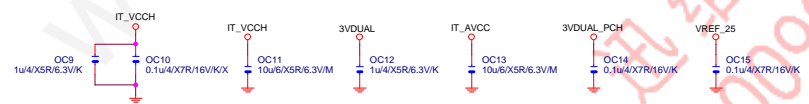
SIO 18V



MB ID



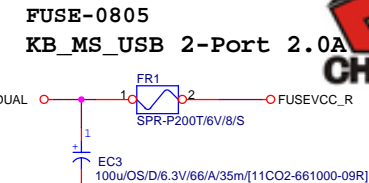
SIO CAP



Gigabyte Technology			
PCH GPIO , CTRL , AUDIO			
File	Document Number	GA-H81M-D2W WG	Rev 1.01
Date:	Tuesday, December 31, 2013	Sheet 17 of 33	

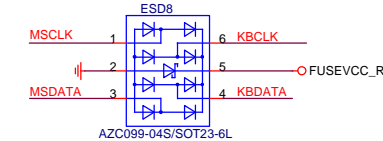


USB2.0 PWR

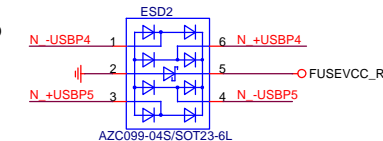


Close to connector

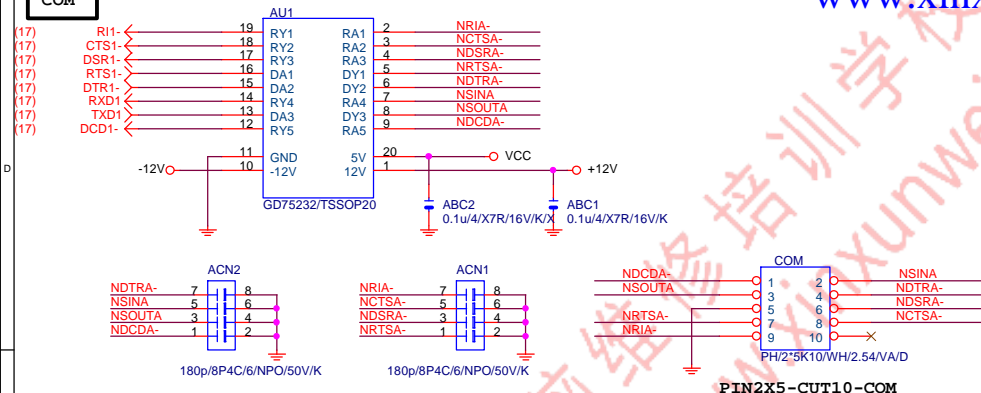
KB/MS ESD



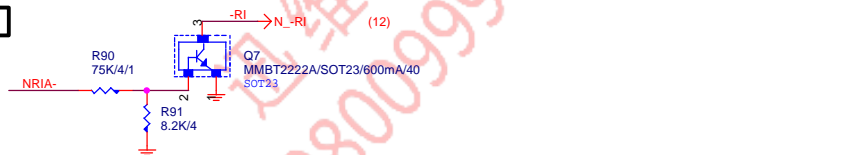
USB2.0 ESD



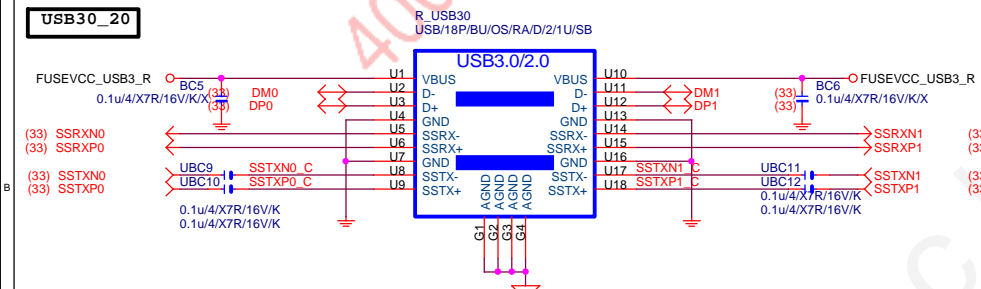
COM



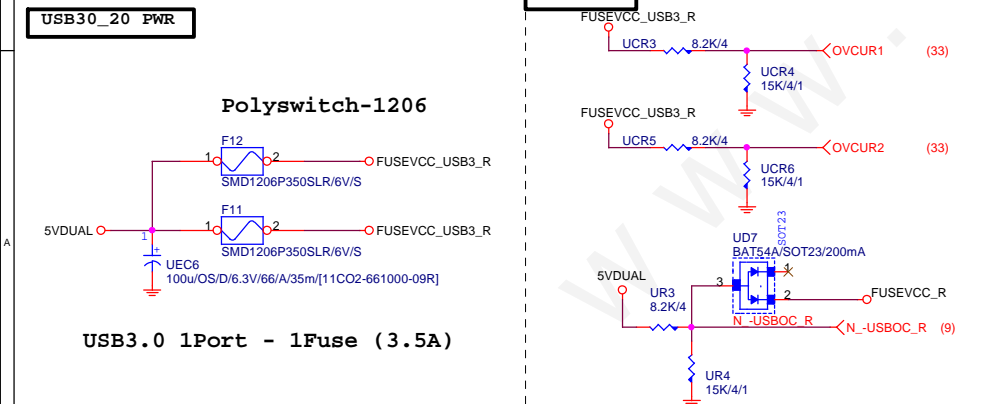
COM RI



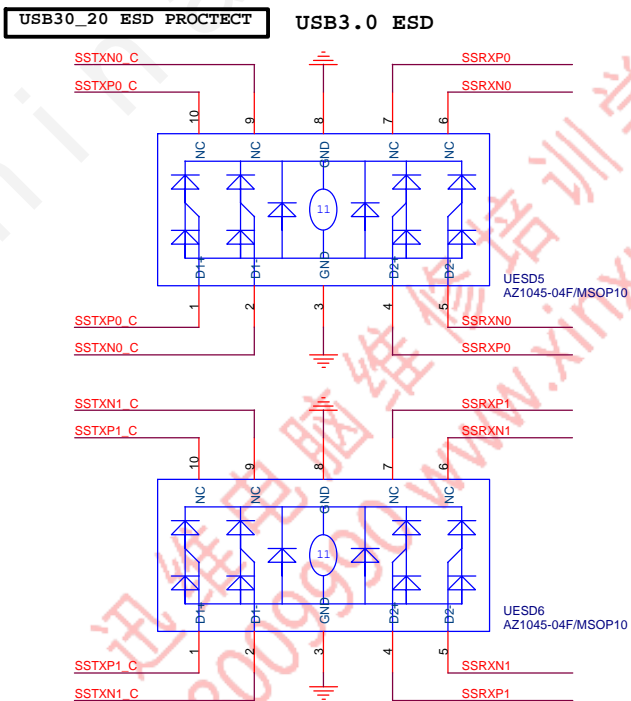
USB30_20



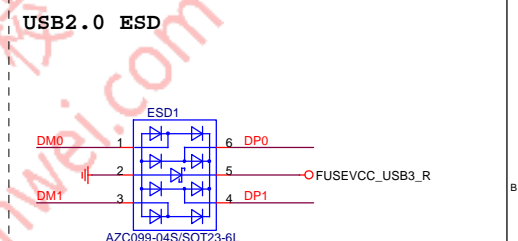
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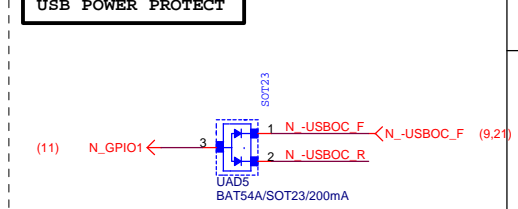
USB30_20 ESD PROTECT



USB2.0 ESD



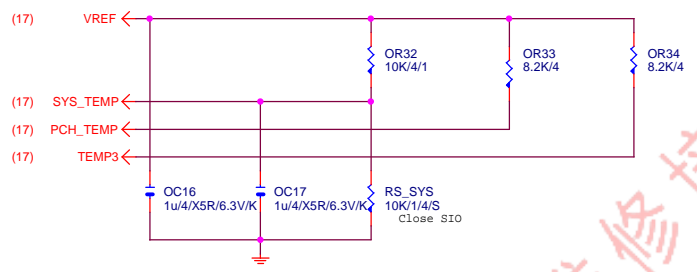
USB POWER PROTECT



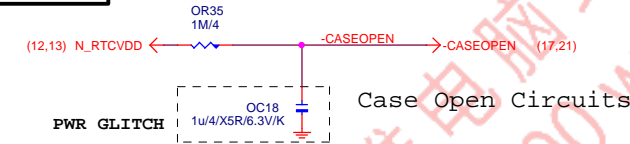
Gigabyte Technology

Title				COM,-RI,KB_USB,USB_ESATA,-PROCHOT	
Size	Custom	Document Number	GA-H81M-D2W WG		Rev 1.01
Date:	Tuesday, December 31, 2013	Sheet	18	of	33

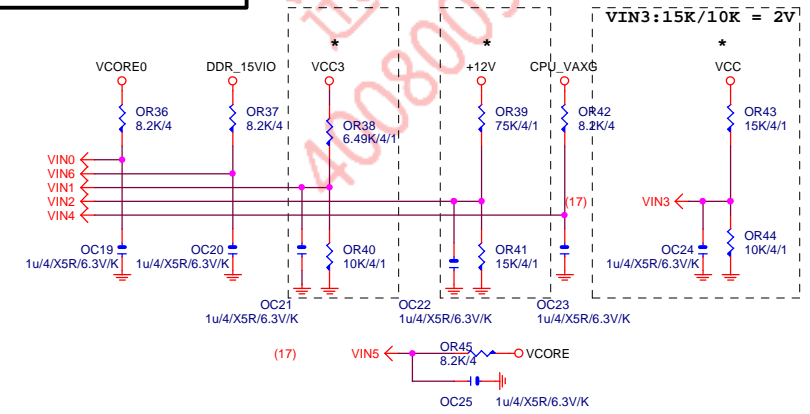
TEMP H/W MONITOR



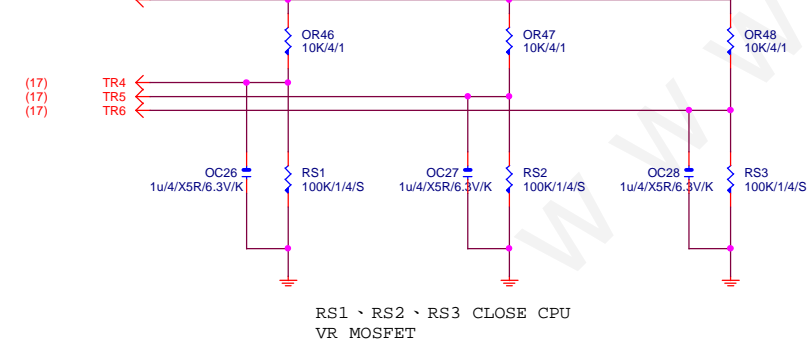
CASE OPEN



VOLTAGE-- H/W MONITOR

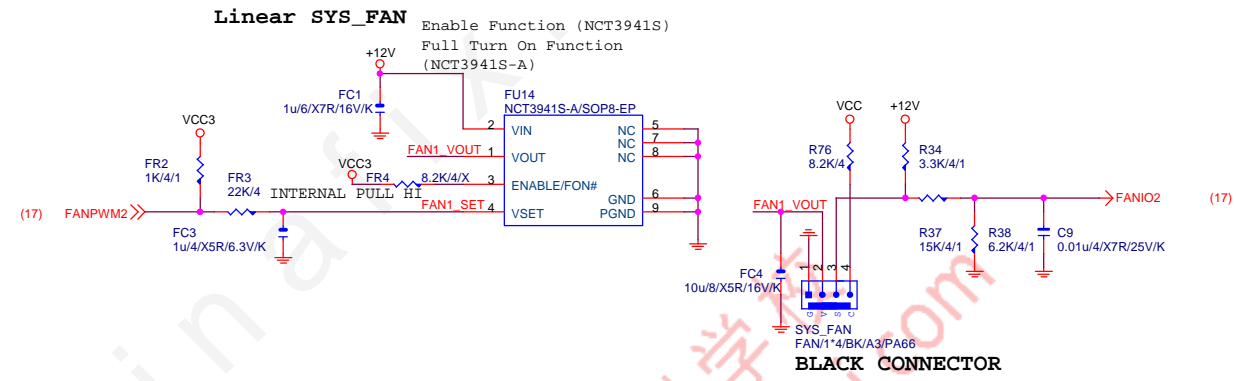


TEMP H/W MONITOR

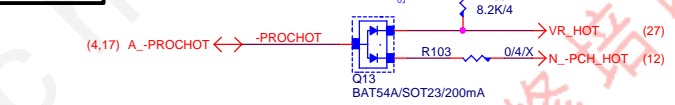


RS1、RS2、RS3 CLOSE CPU VR MOSFET

SYS SMART FAN

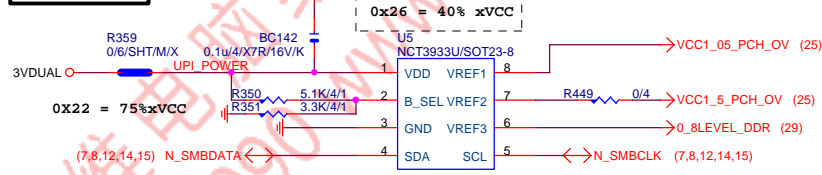


-PROHOT



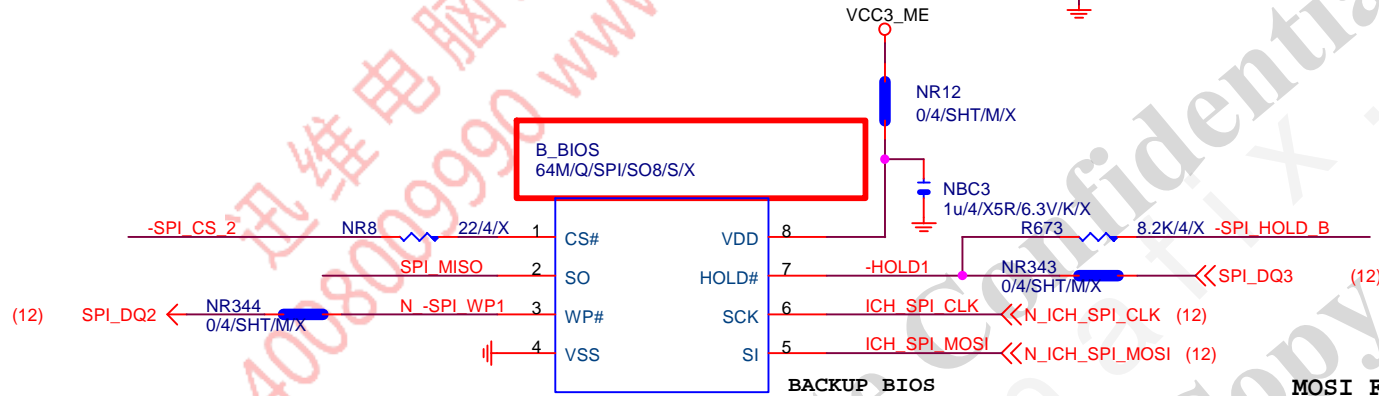
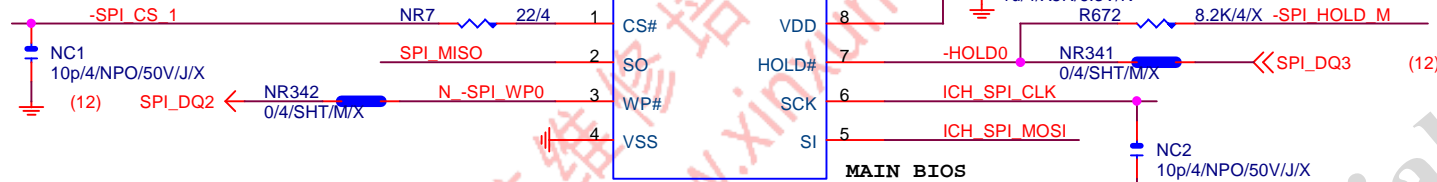
接pwm feedback pin

OV NCT3933



NCT3933	0X2A	0X20	0X22
VREF1	DDRVTT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

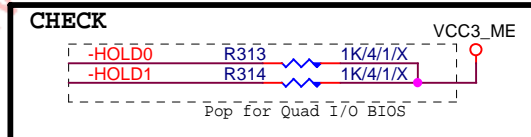
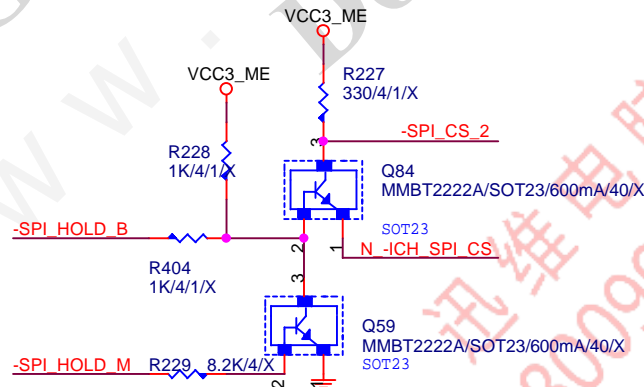
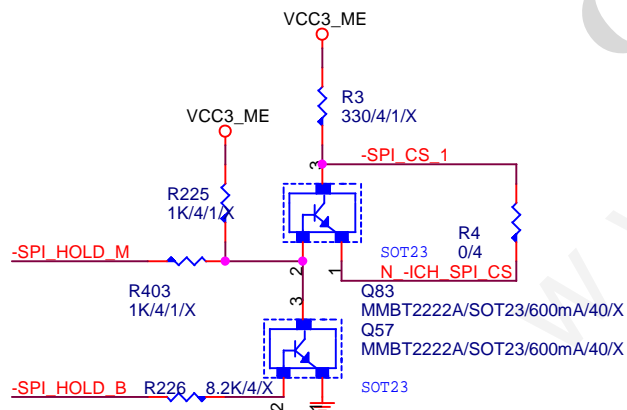
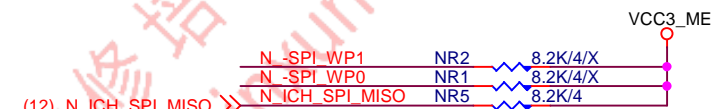
Gigabyte Technology			
Title HWM,FAN CTRL,OV			
Size Custom	Document Number	GA-H81M-D2W WG	
Date: Tuesday, December 31, 2013	Sheet	19	of 33



BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

MOSI For DMI RX Termination Voltage



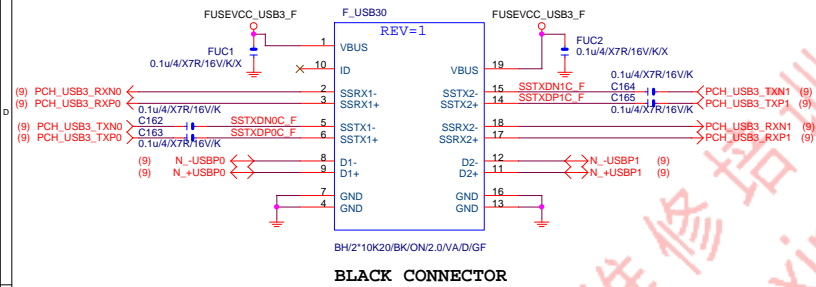
Gigabyte Technology

DUAL BIOS

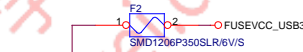
Title		
GA-H81M-D2W WG		
Size Custom	Document Number	Rev 1.01
Date: Tuesday, December 31, 2013	Sheet 20 of 33	



F_USB30

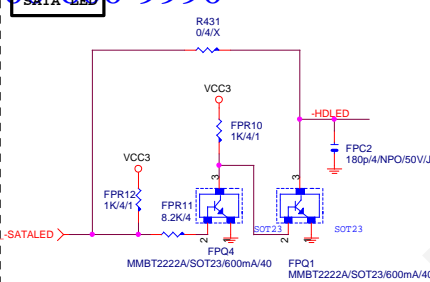


Polyswitch-1206

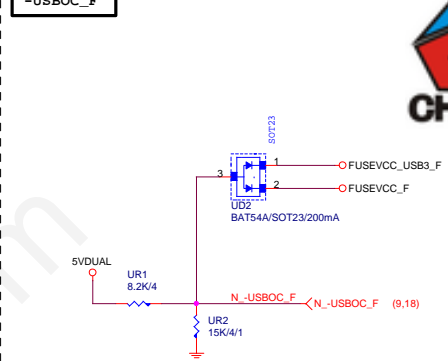


USB3.0 1Port - 1Fuse (3.5A)

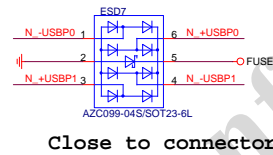
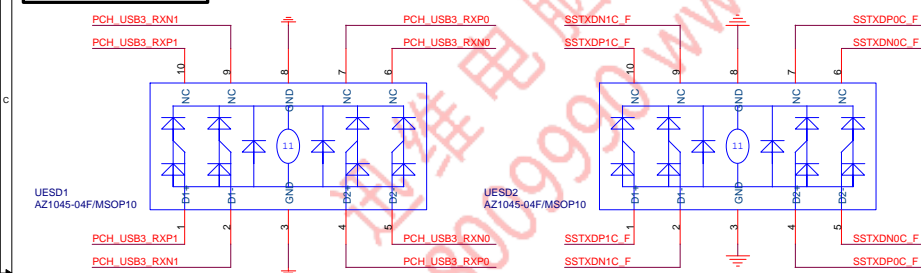
SATA_LED



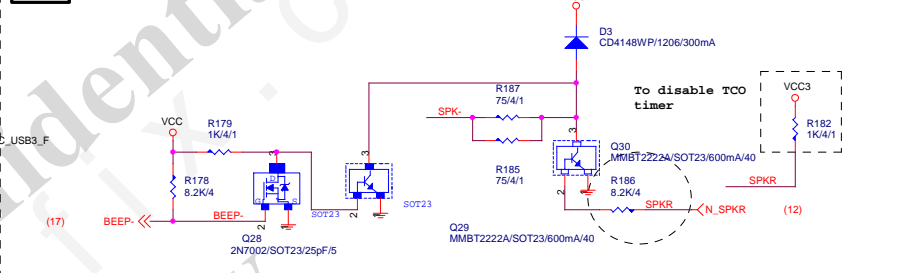
-USB0C_F



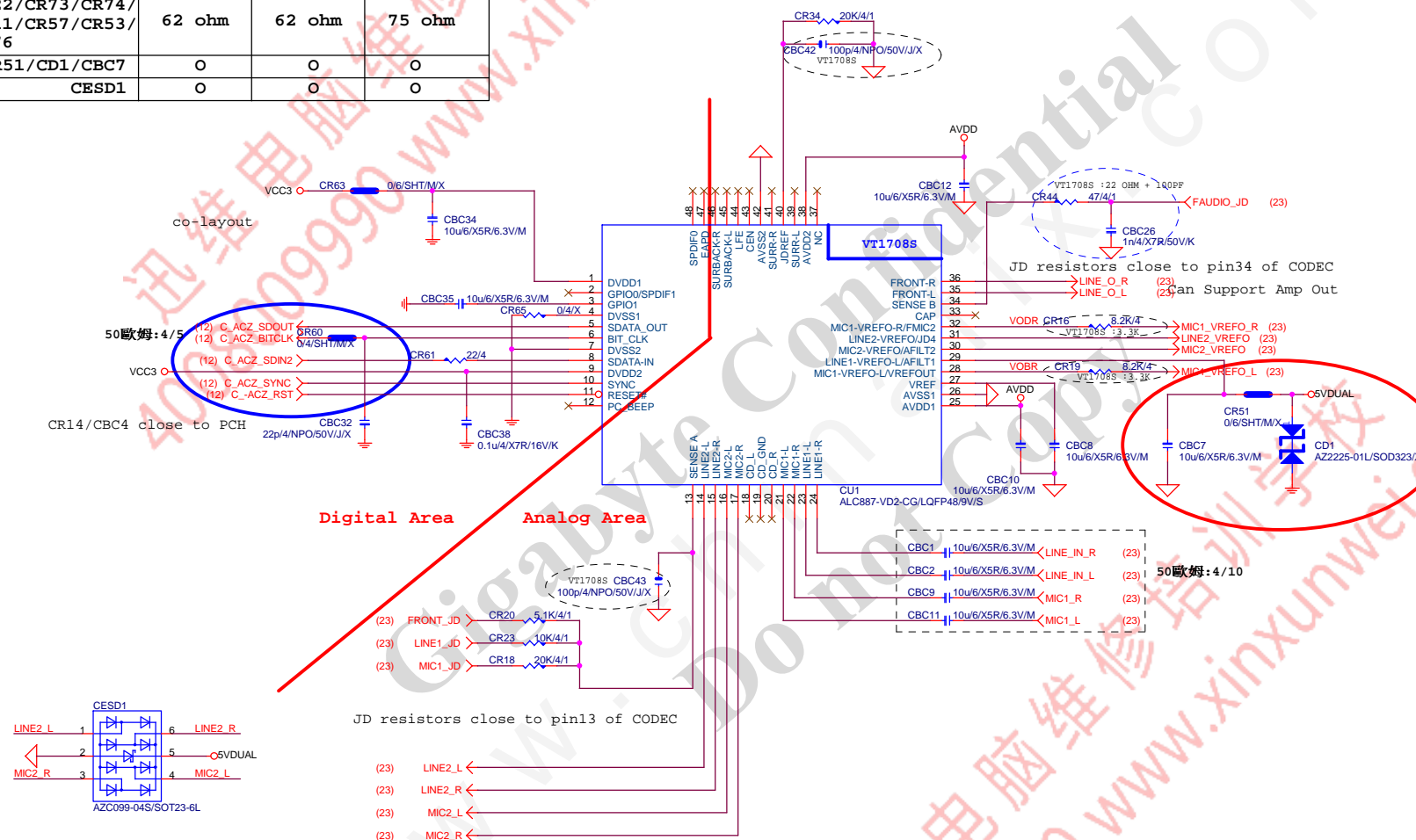
F_USB30 ESD PROTECT



SPKR

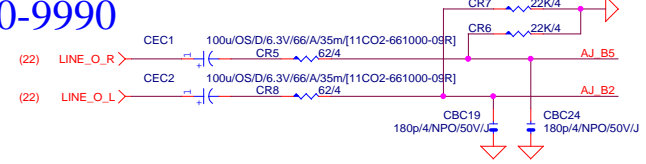
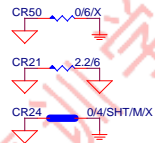


	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O



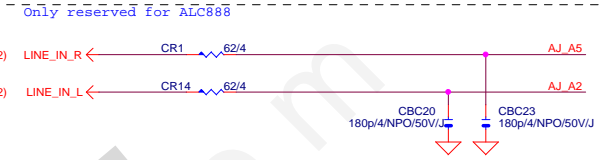


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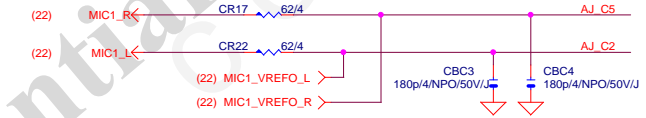
LINE-IN

Verify MIC function
in LINE-in

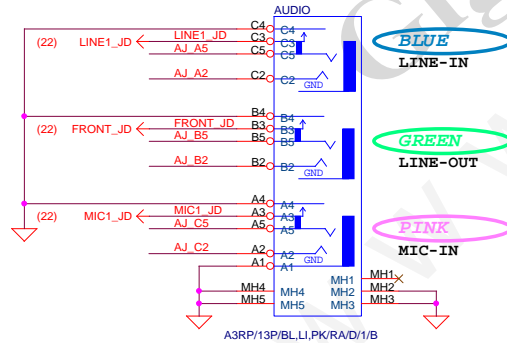


For 889A/888

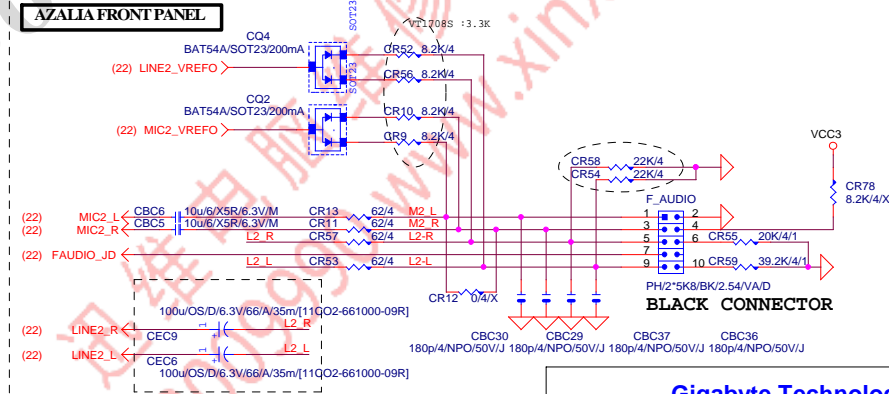
MIC-IN



SPDIF_OUT

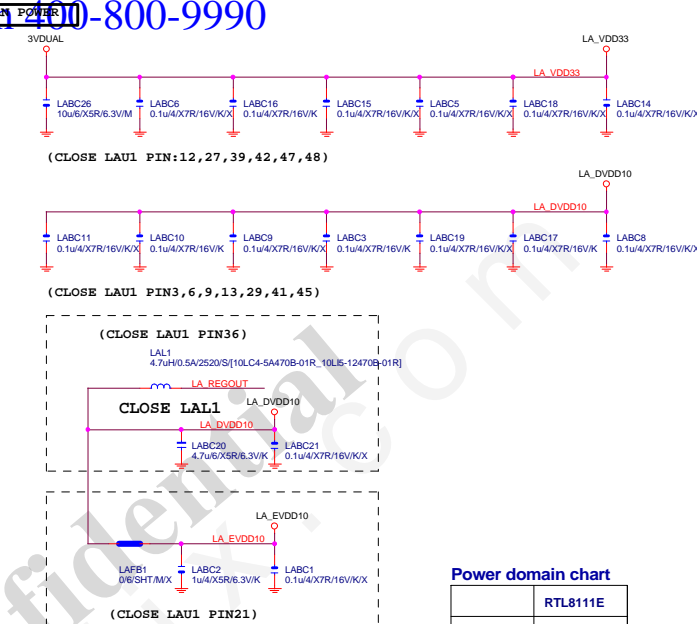


AZALIA FRONT PANEL

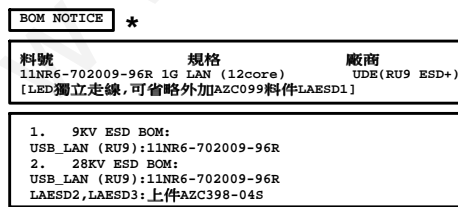
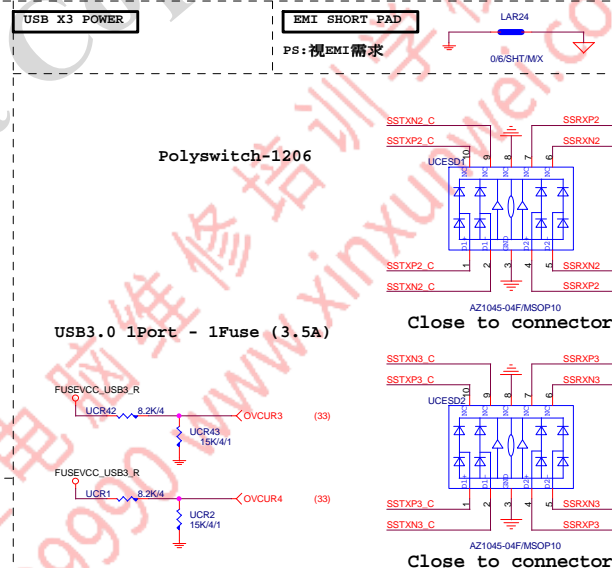


Gigabyte Technology

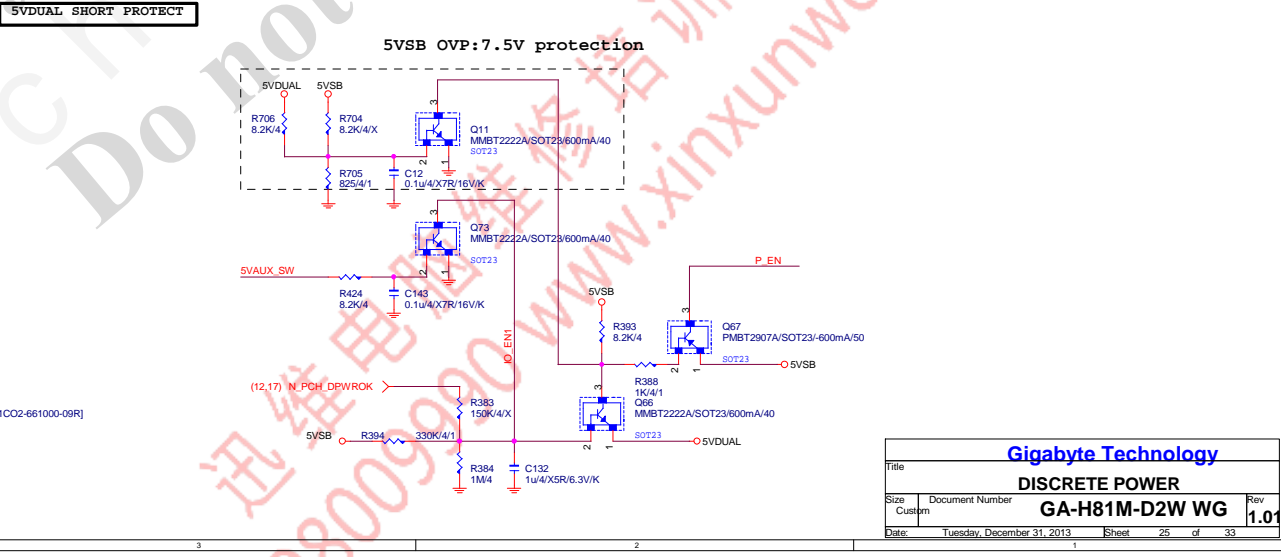
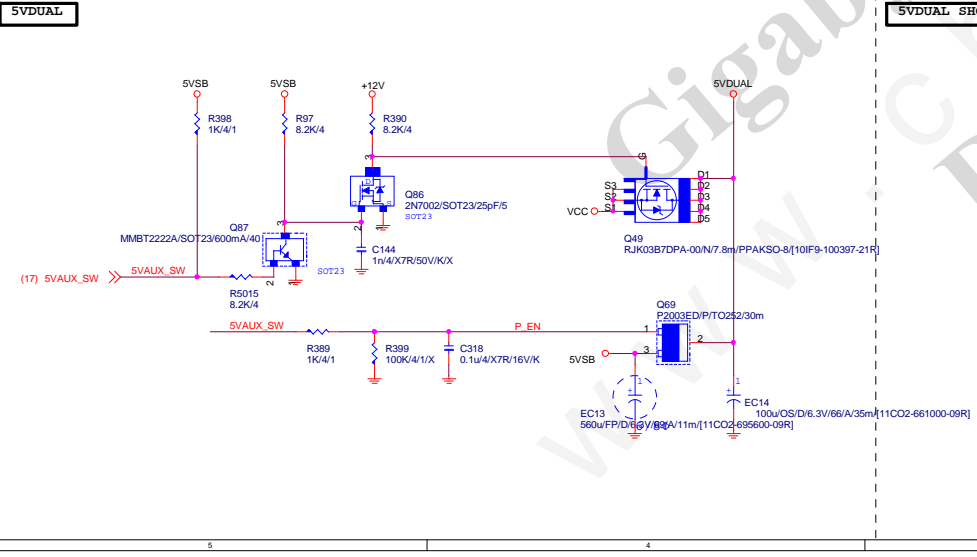
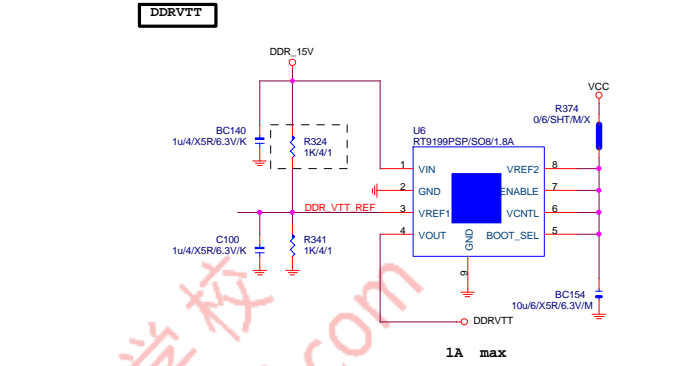
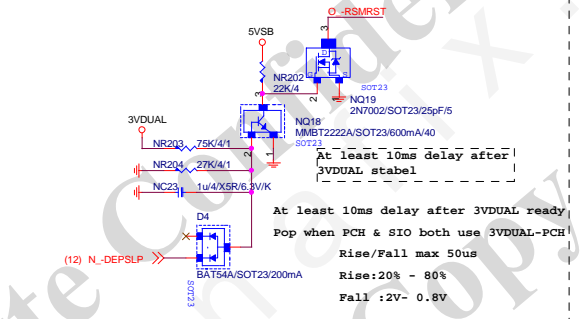
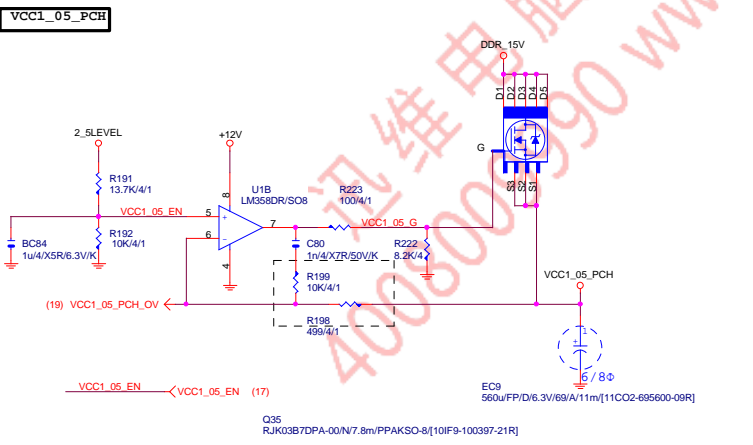
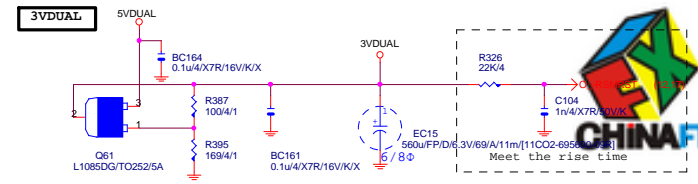
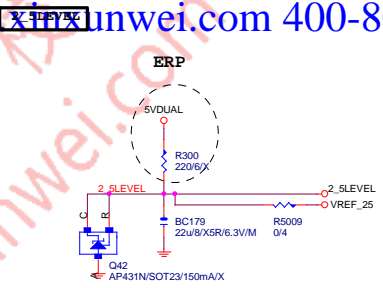
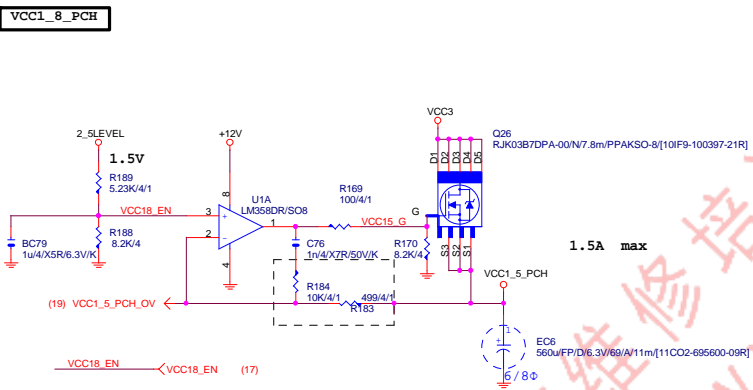
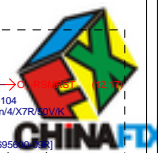
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AUDIO JACK			
Size	Document Number	GA-H81M-D2W WG	
Custom			Rev 1.01
Date:	Tuesday, December 31, 2013	Sheet	23 of 33



	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



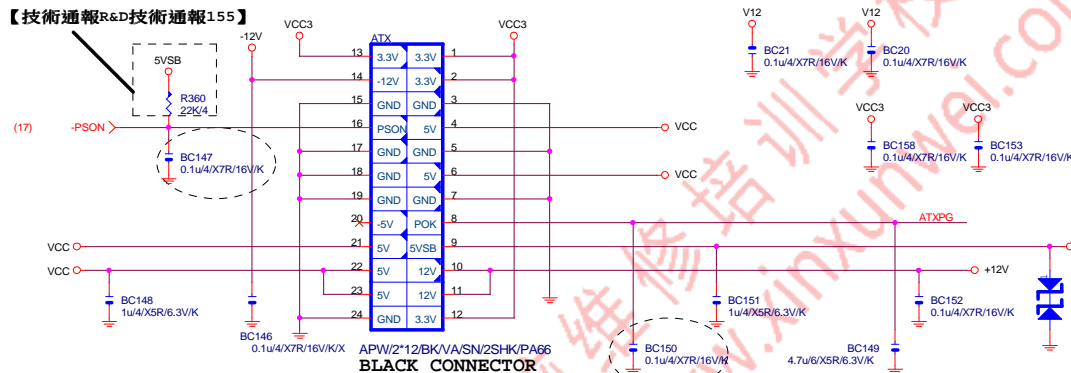
Gigabyte Technology			
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Realtek RTL8111G			
Size	Document Number	GA-H81M-D2W WG	Rev
Custom			1.01
Date:	Tuesday, December 31, 2013	Sheet	24 of 33



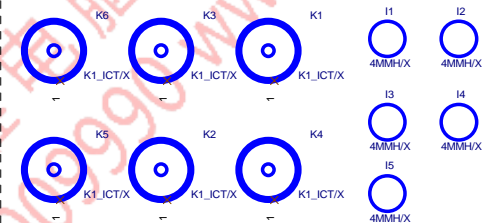
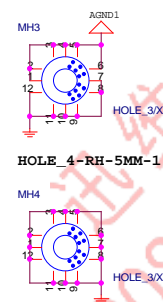
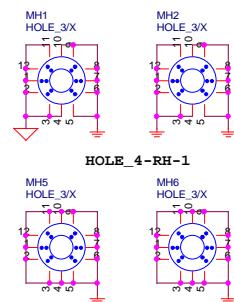
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DISCRETE POWER			
Size	Document Number	GA-H81M-D2W WG	
Custom		Rev 1.01	
Date:	Tuesday, December 31, 2013	Sheet	25 of 33

ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】



BLACK CONNECTOR

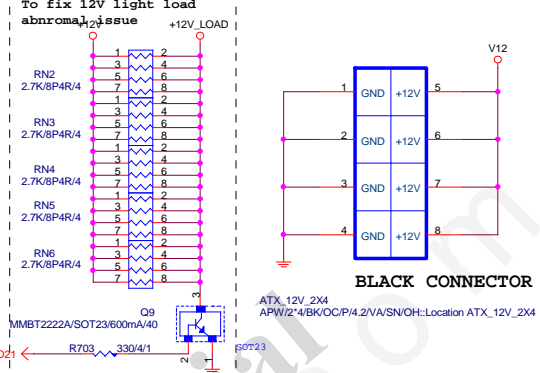


TPM

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ATXX4 POWER CONNECTOR

【技術通報R&D技術通報158】

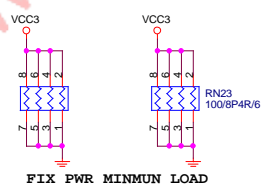
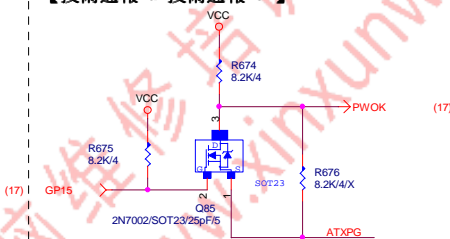


BLACK CONNECTOR

ATX 12V 2X4
APW/2*4BK/OC/P/4.2V/A/SN/OH: Location ATX_12V_2X4

PWOK PATCH

【技術通報R&D技術通報154】



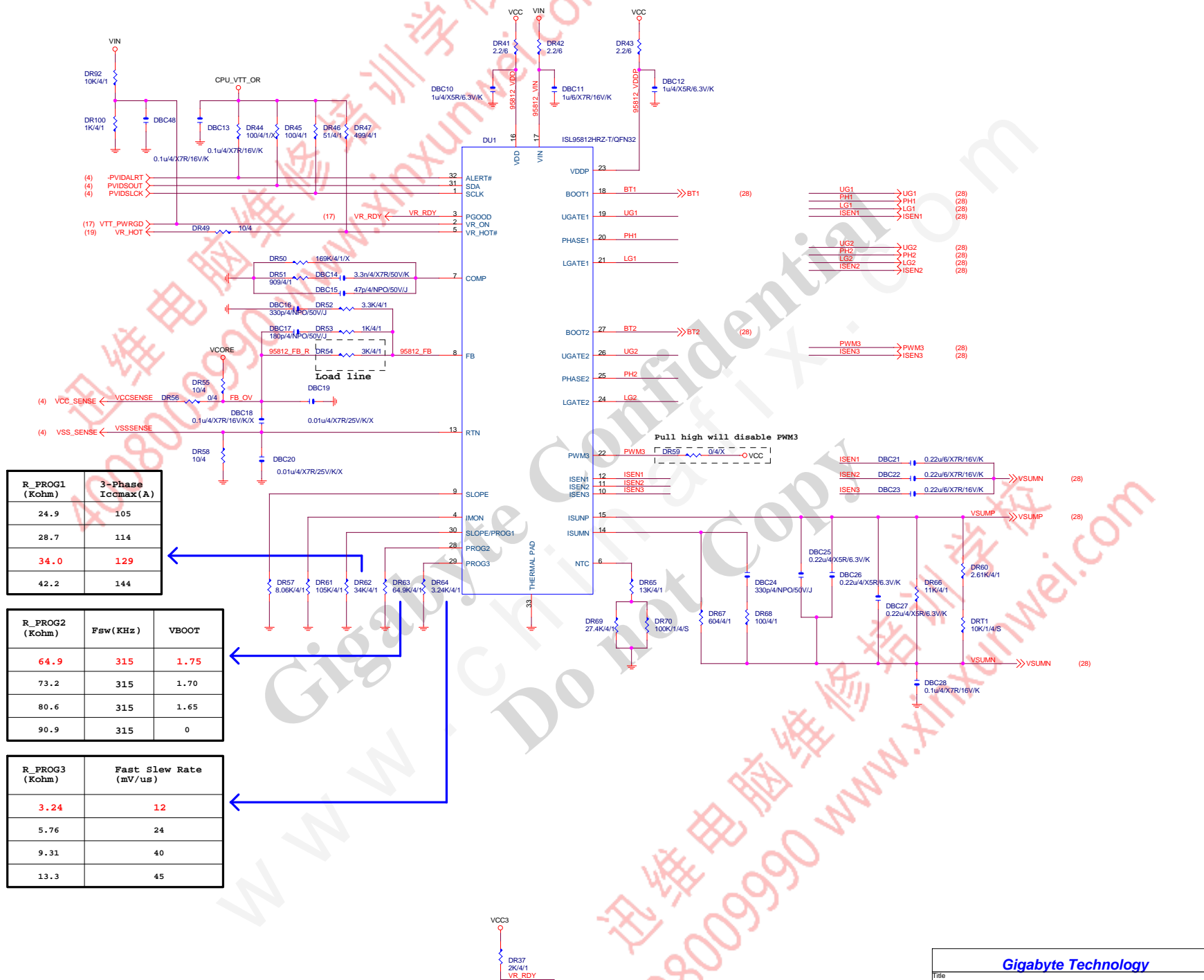
Gigabyte Technology

ATX CONNECTOR

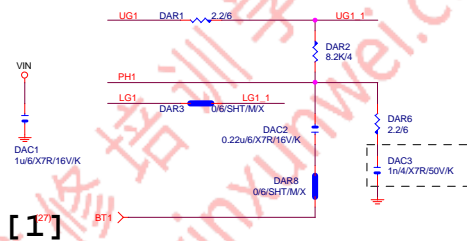
GA-H81M-D2W WG

Rev 1.01

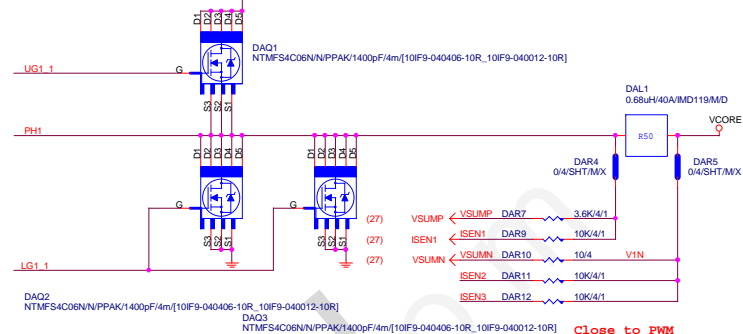




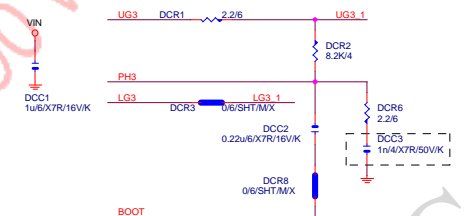
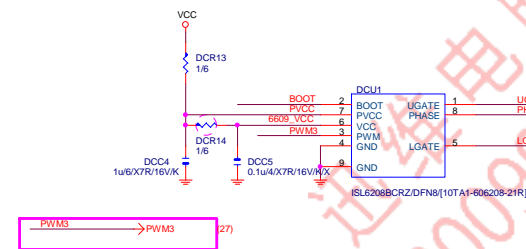
PHASE 1



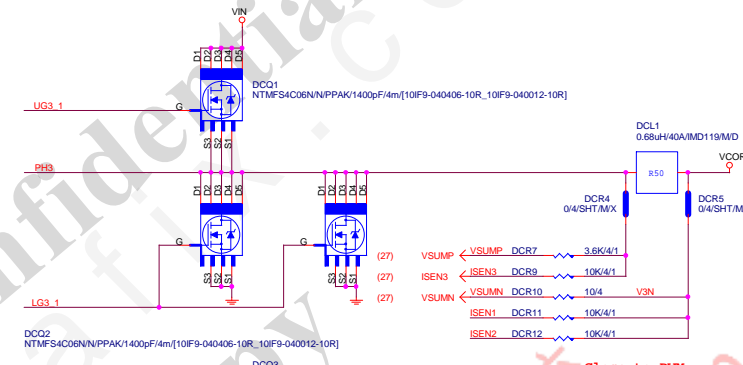
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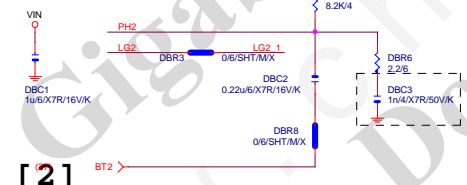
PHASE 3



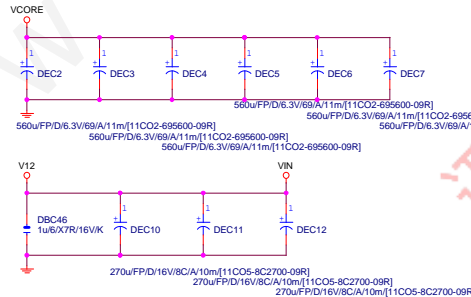
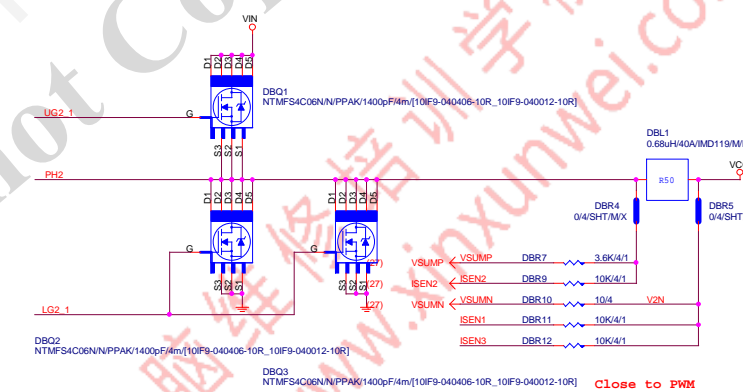
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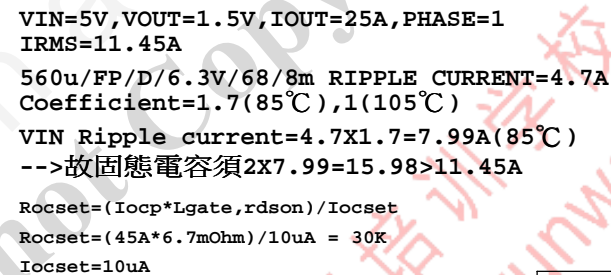



PHASE 2



[2]





			
Title			
DDR POWER			
Size	Document Number	GA-H81M-D2W WG	Rev
Custom			1.01
Date:	Tuesday, December 31, 2013	Sheet	29 of 33

VCC1_05_ME

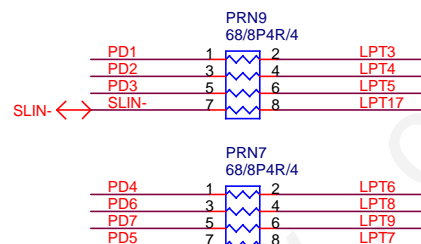
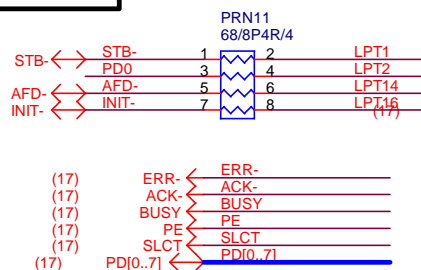
【技術通報R&D技術通報156】
(RICHTEK), (NUVOTON), (EMC)做共用
PIN7分壓阻值須做修改為100K以上電阻值

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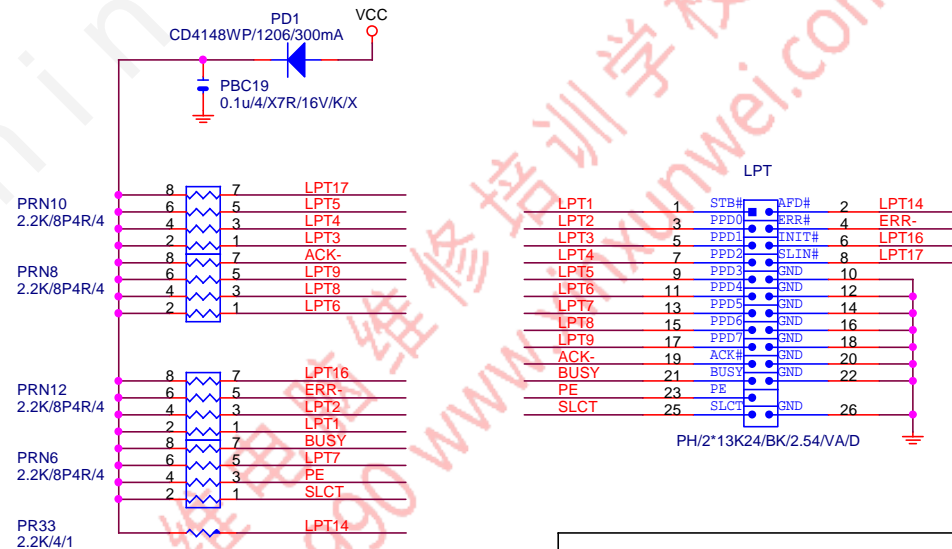
VCC3_ME



LPT PORT

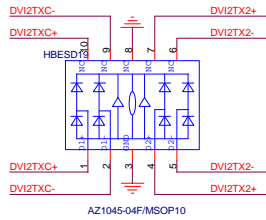


【技術通報R&D技術通報151】
33ohm Change to 68ohm

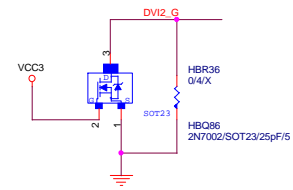


Gigabyte Technology

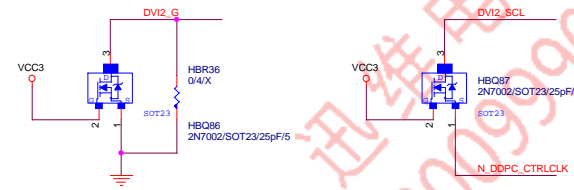
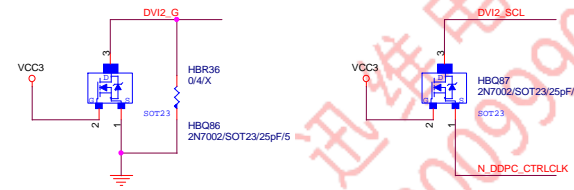
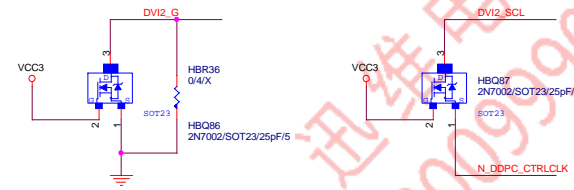
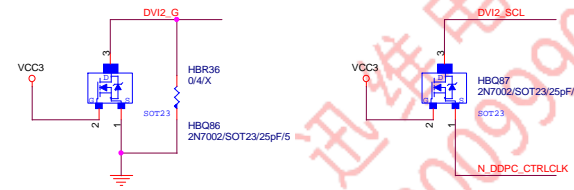
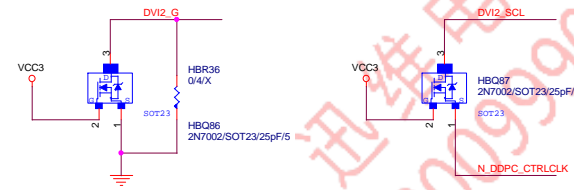
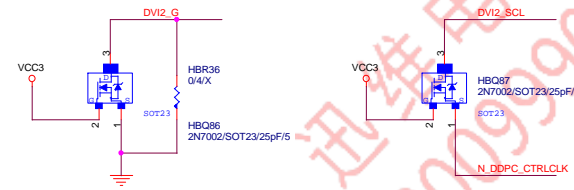
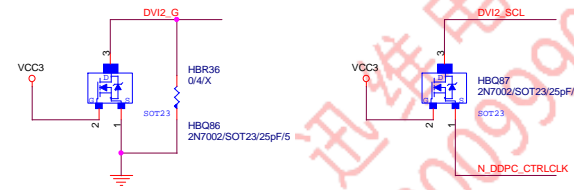
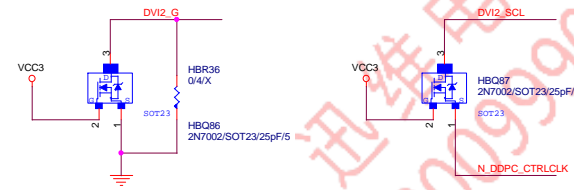
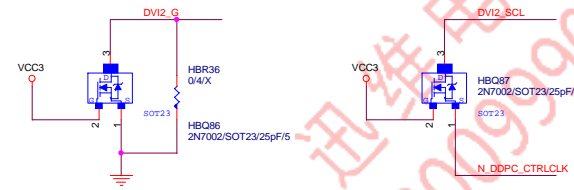
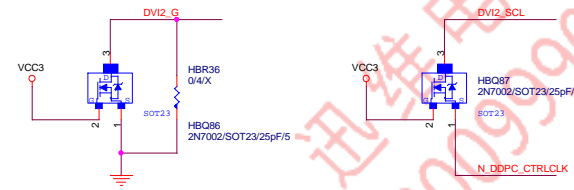
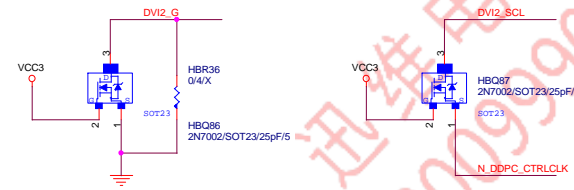
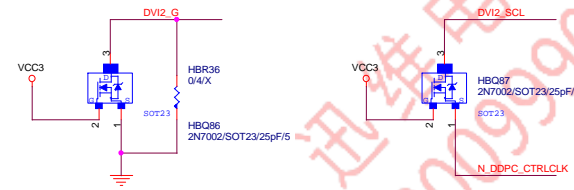
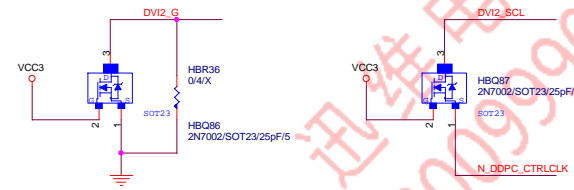
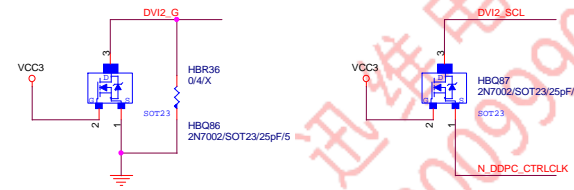
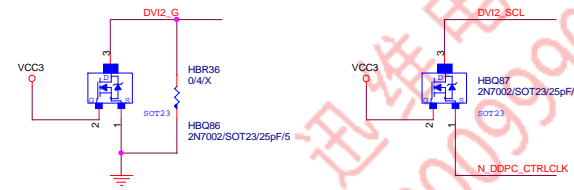
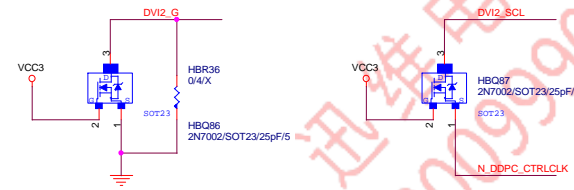
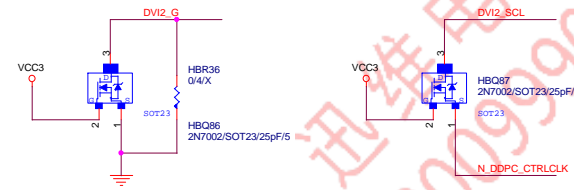
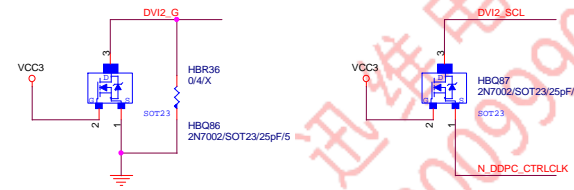
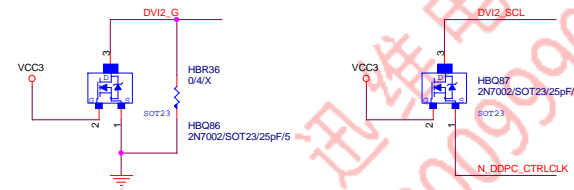
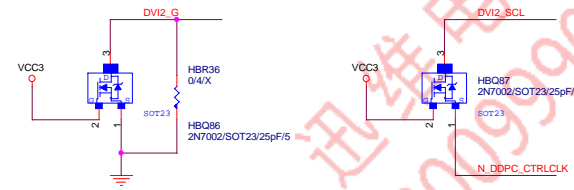
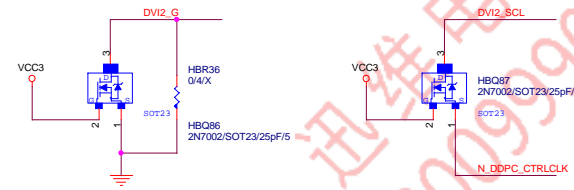
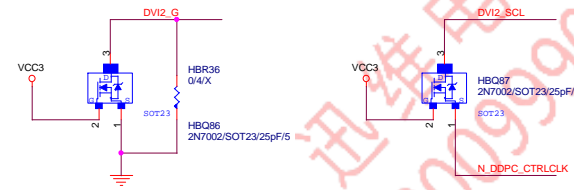
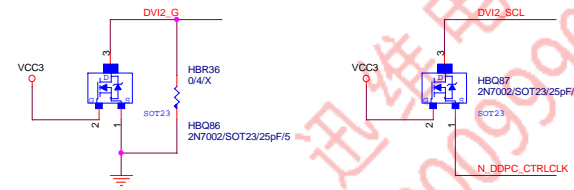
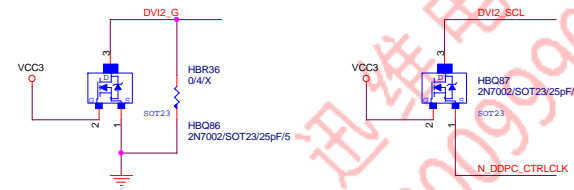
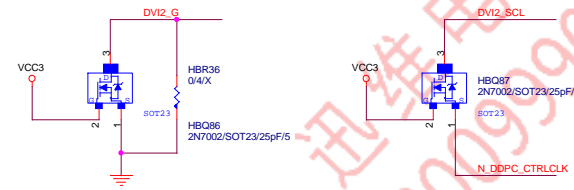
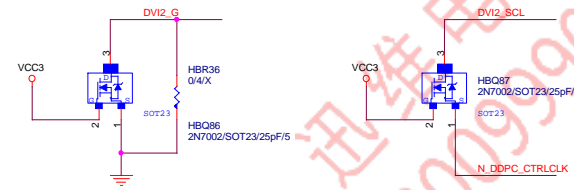
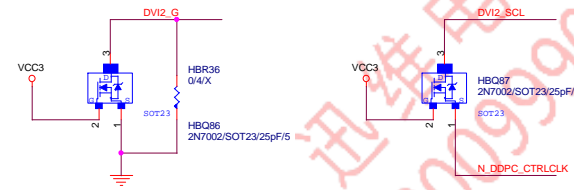
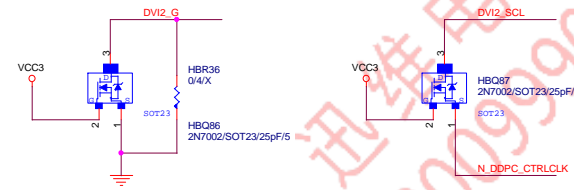
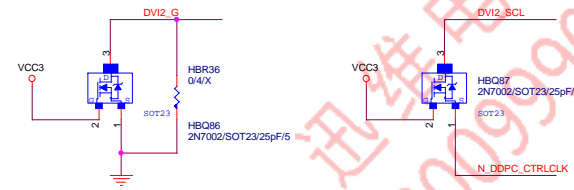
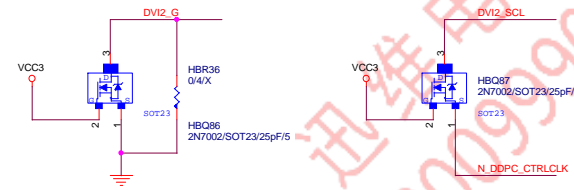
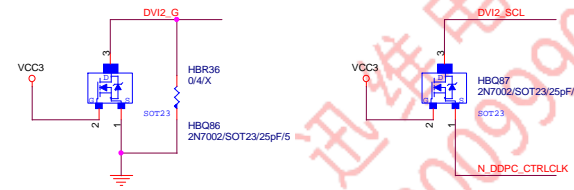
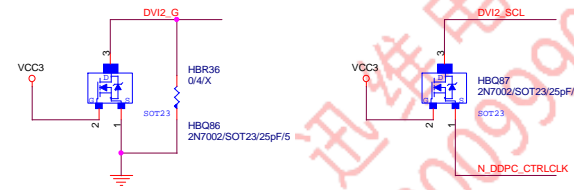
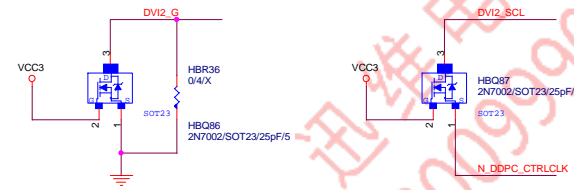
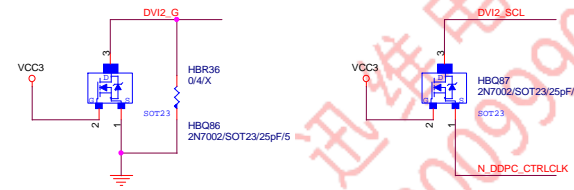
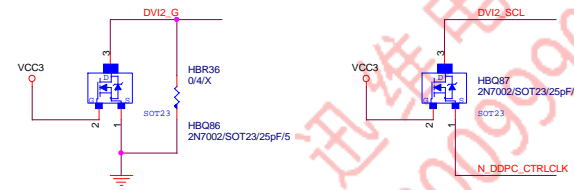
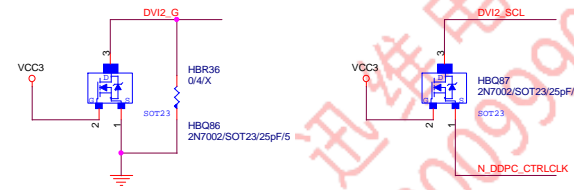
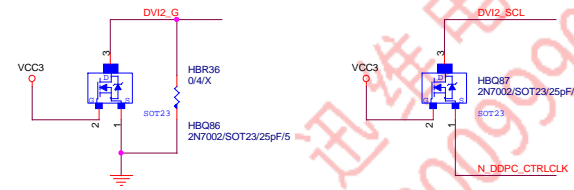
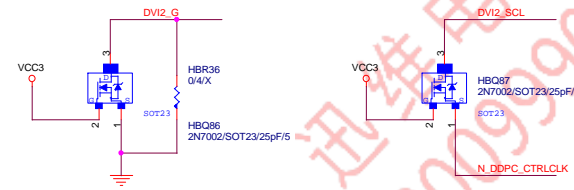
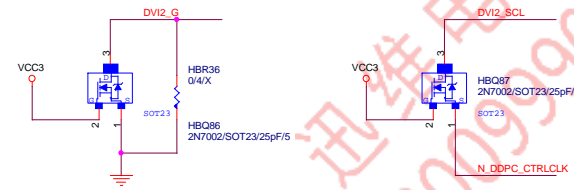
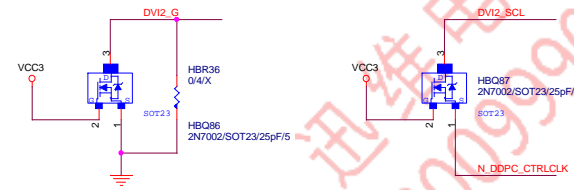
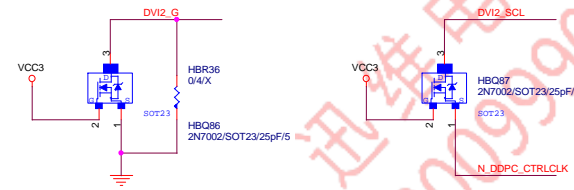
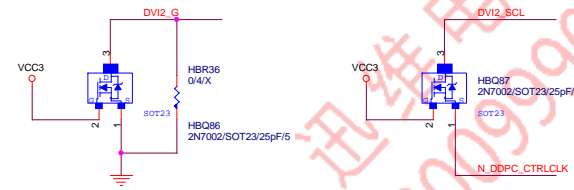
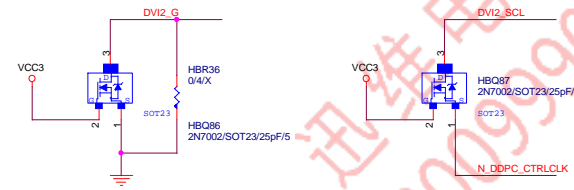
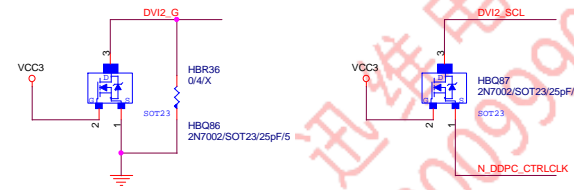
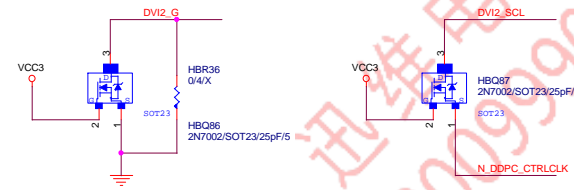
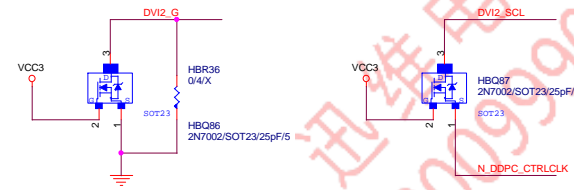
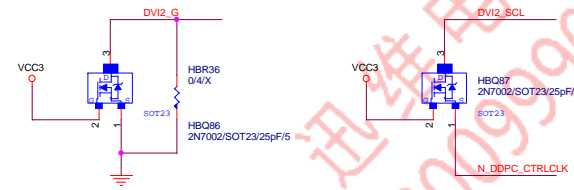
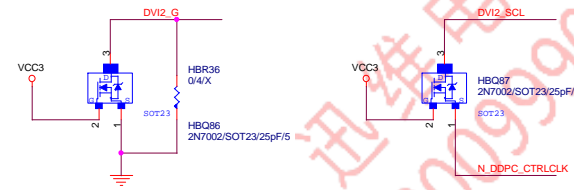
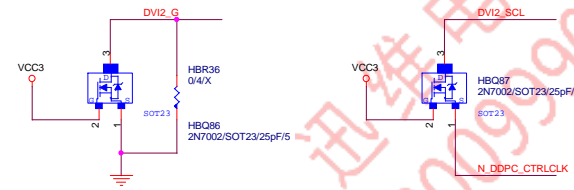
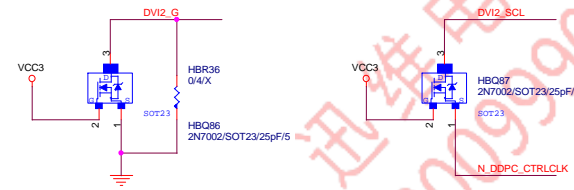
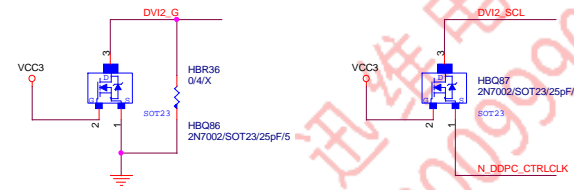
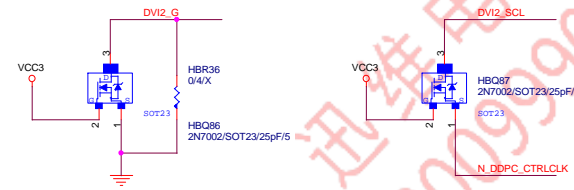
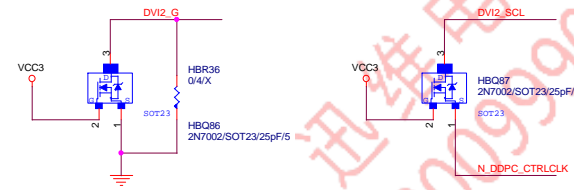
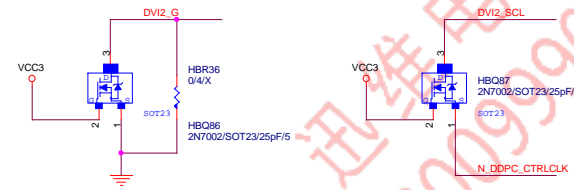
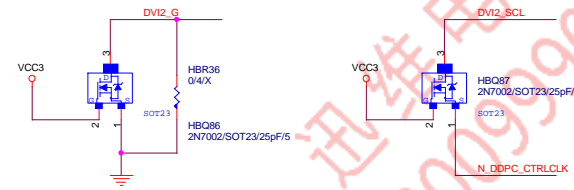
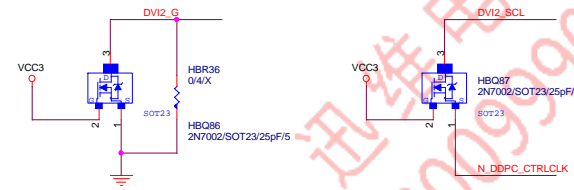
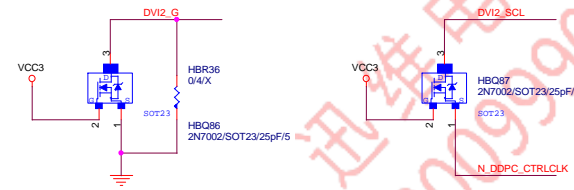
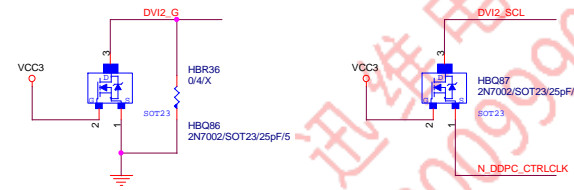
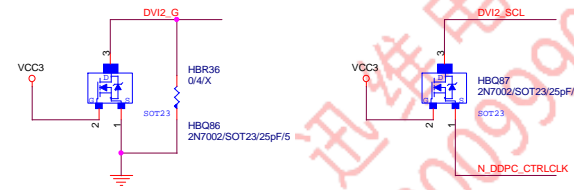
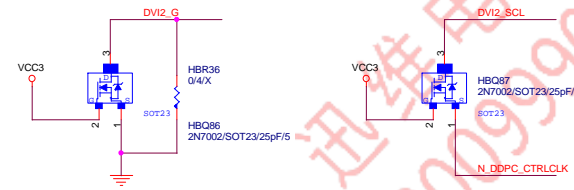
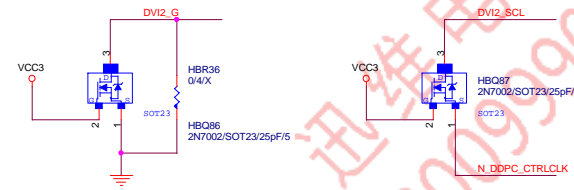
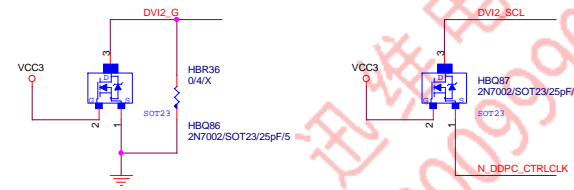
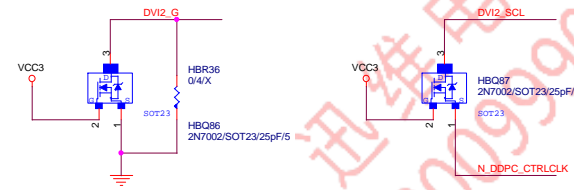
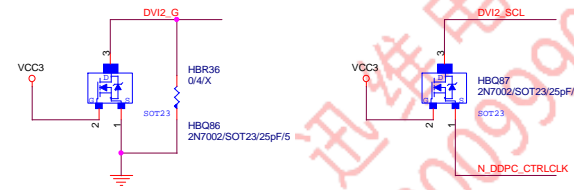
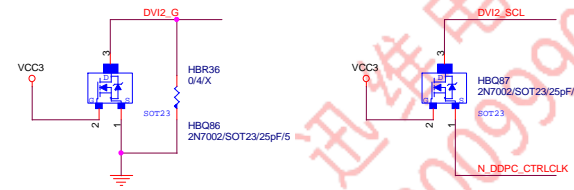
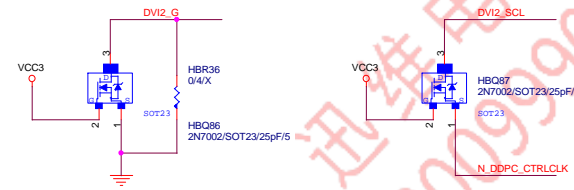
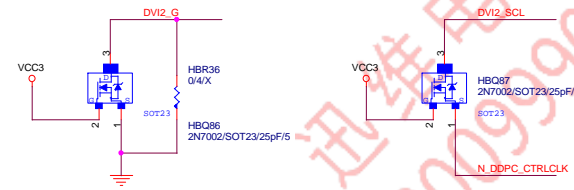
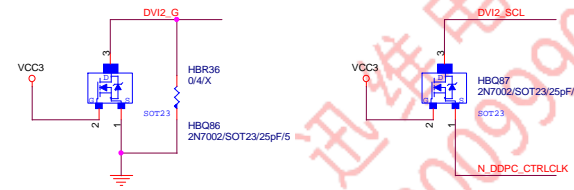
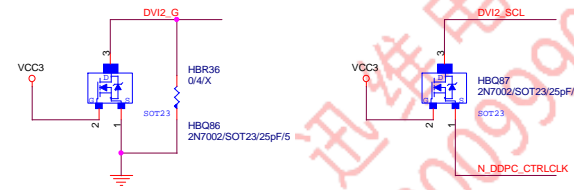
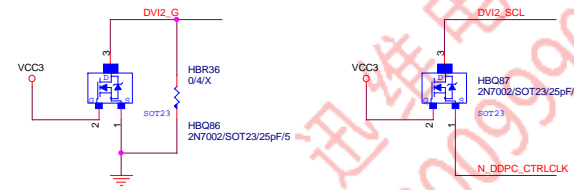
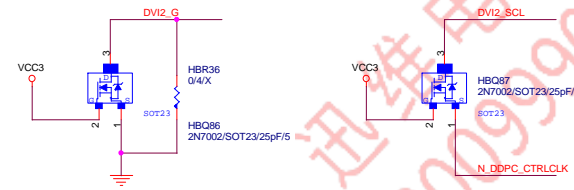
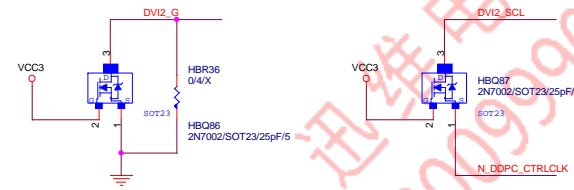
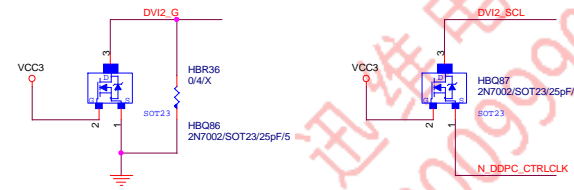
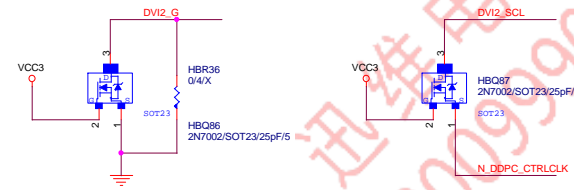
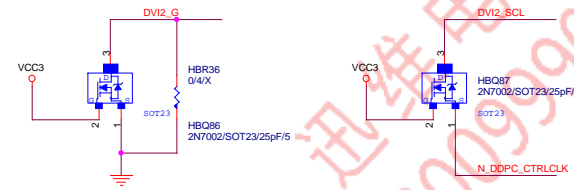
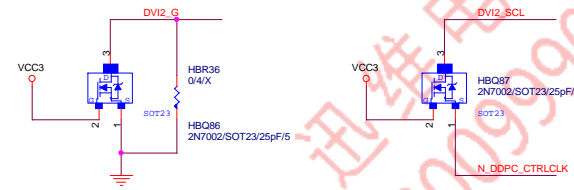
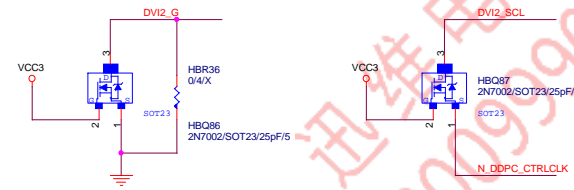
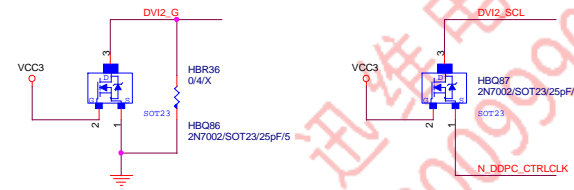
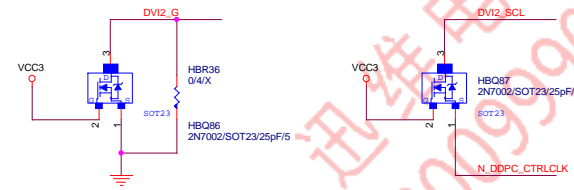
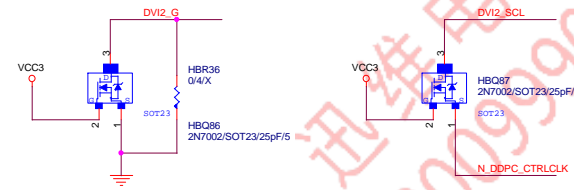
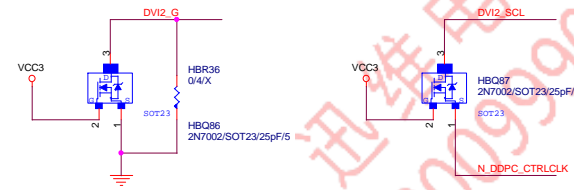
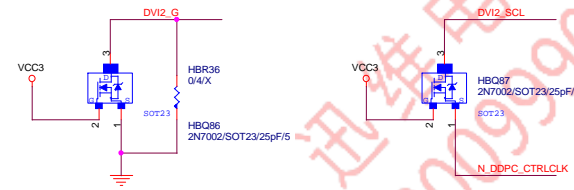
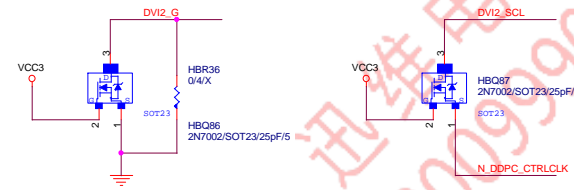
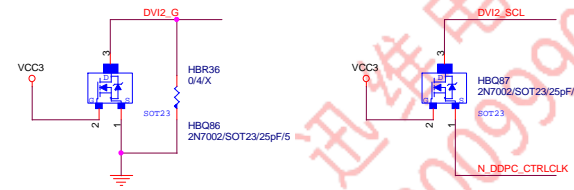
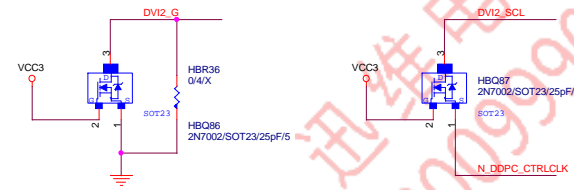
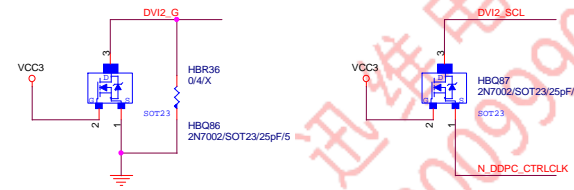
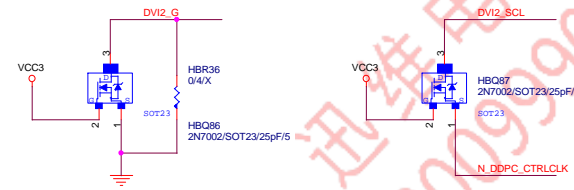
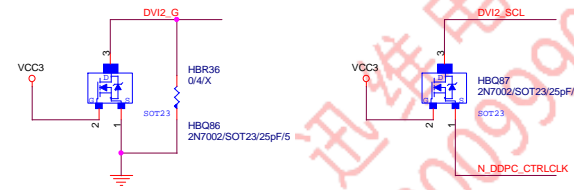
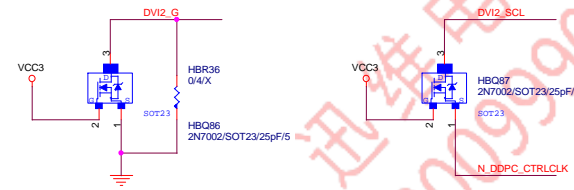
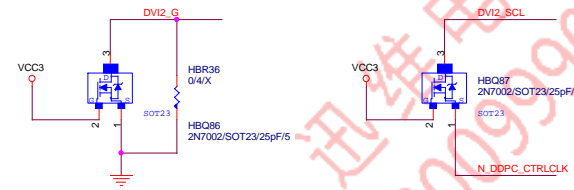
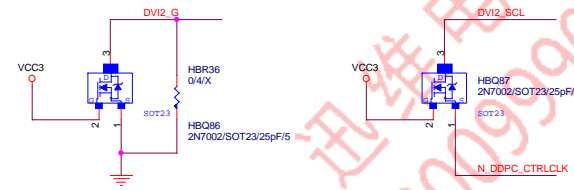
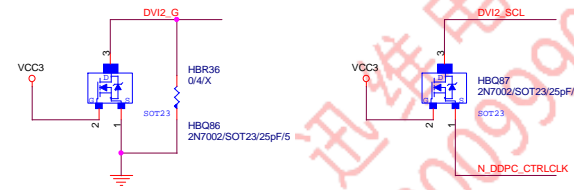
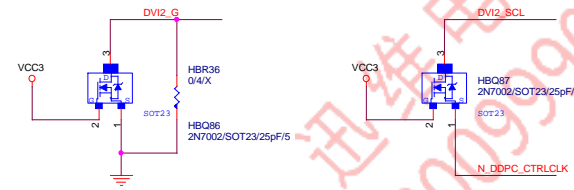
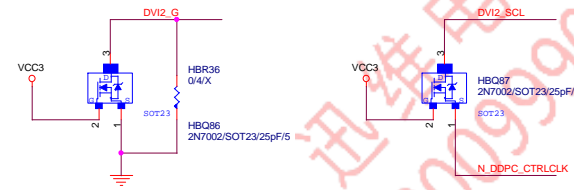
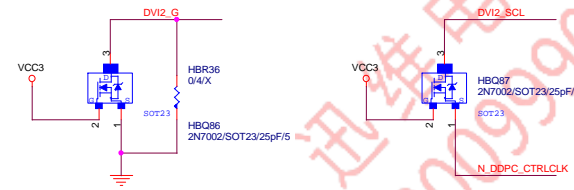
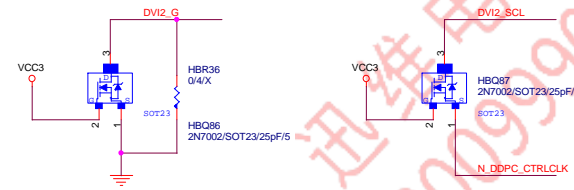
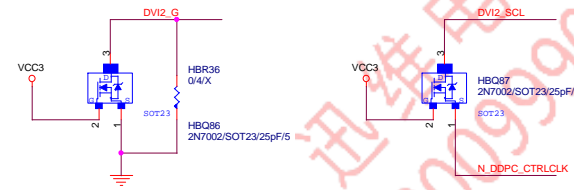
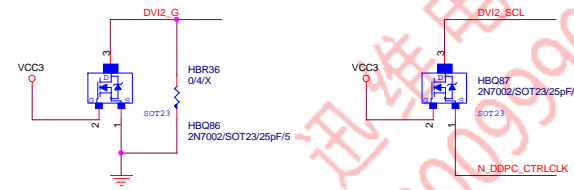
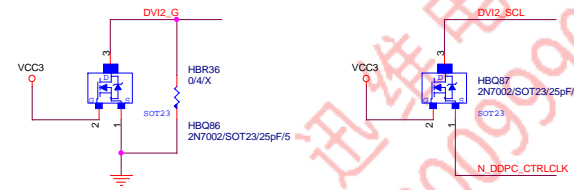
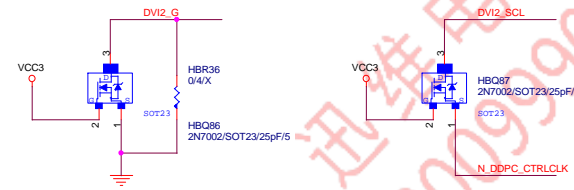
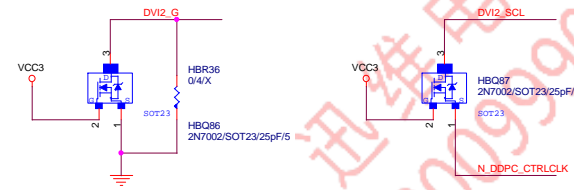
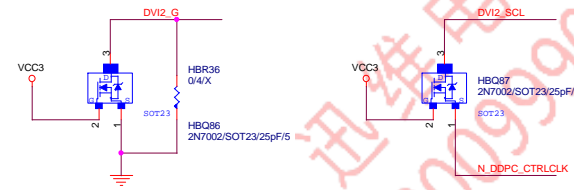
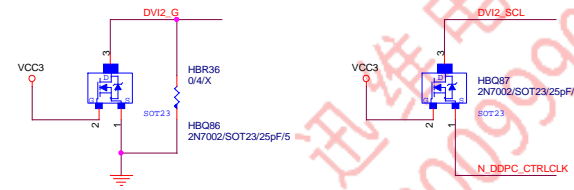
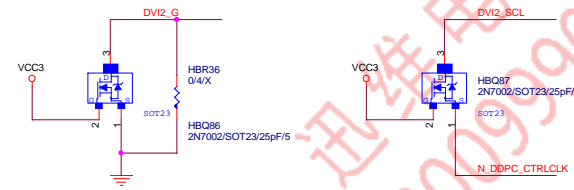
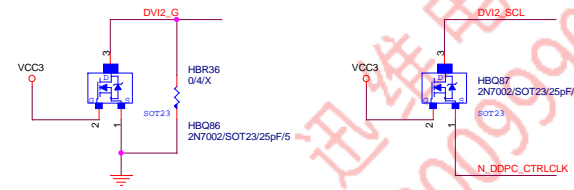
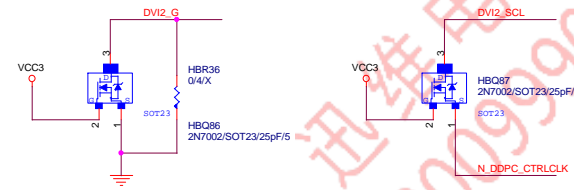
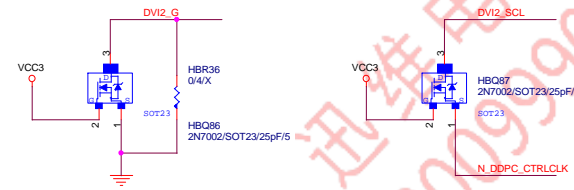
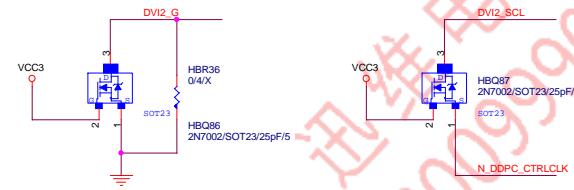
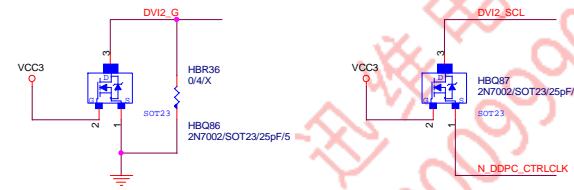
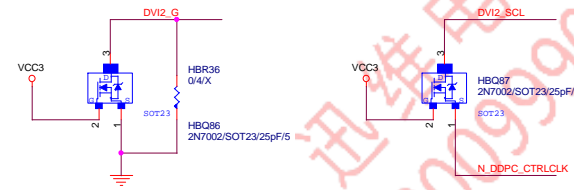
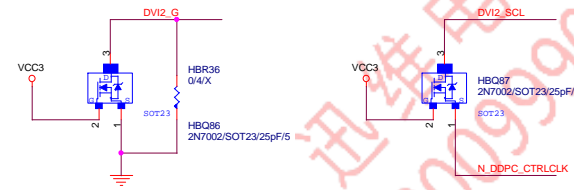
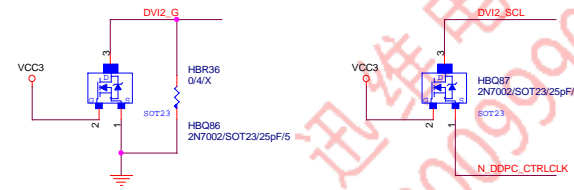
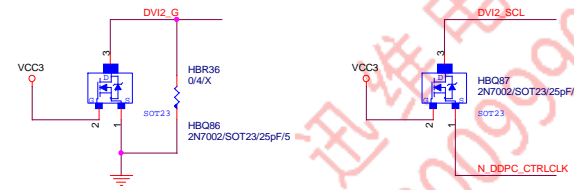
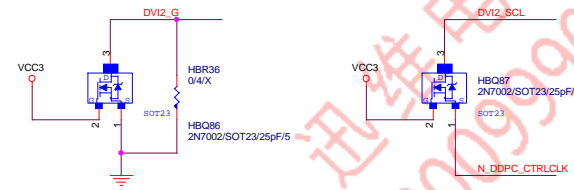
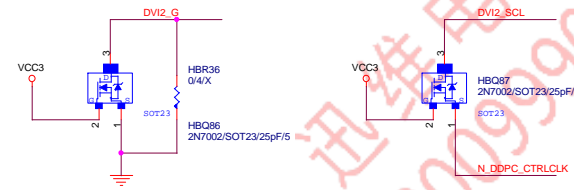
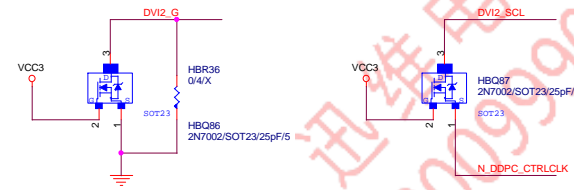
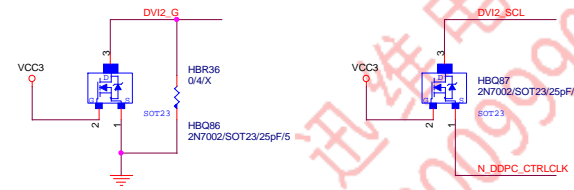
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Date:	Tuesday, December 31, 2013	Sheet 30	of 33



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Custom	GA-H81M-D2W WG		1.0
Date:	Tuesday, December 31, 2013	Sheet	32 of 33

